

# SyGuS Techniques in the Core of an SMT Solver

Andrew Reynolds

SYNT Workshop

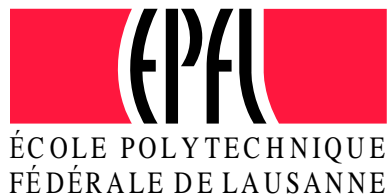
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OF IOWA

# Acknowledgements

- Thanks to past and present collaborators:
  - EPFL : Viktor Kuncak
  - University of Iowa : Cesare Tinelli, Arjun Viswanathan
  - Stanford University : Clark Barrett
  - Google : Tim King
  - NYU : Morgan Deters



# SMT Solvers for Synthesis

- Act as *subroutines* for automated synthesis tasks
- More recently, instrumented as *stand-alone tools* for synthesis
  - SMT solver CVC4 has entered SyGuS comp 2015, 2016, 2017

[Reynolds et al CAV2015]



# In This Talk

- Synthesis conjectures:

$$\exists f . \forall x . P ( f , x )$$

There exists a function  $f$  for which property  $P$  holds for all  $x$

# In This Talk

- Synthesis conjectures:

$$\exists f . \forall x . P ( f , x )$$

There exists a function  $f$  for which property  $P$  holds for all  $x$

...(optionally) with syntactic restrictions:

$\mathcal{R} :$   $fInt := \mathbf{x} \mid \mathbf{0} \mid \mathbf{1} \mid + (fInt, fInt) \mid \mathbf{ite} (fBool, fInt, fInt)$   
 $fBool := \mathbf{>} (fInt, fInt) \mid \mathbf{=} (fInt, fInt) \mid \mathbf{\neg} (fBool)$

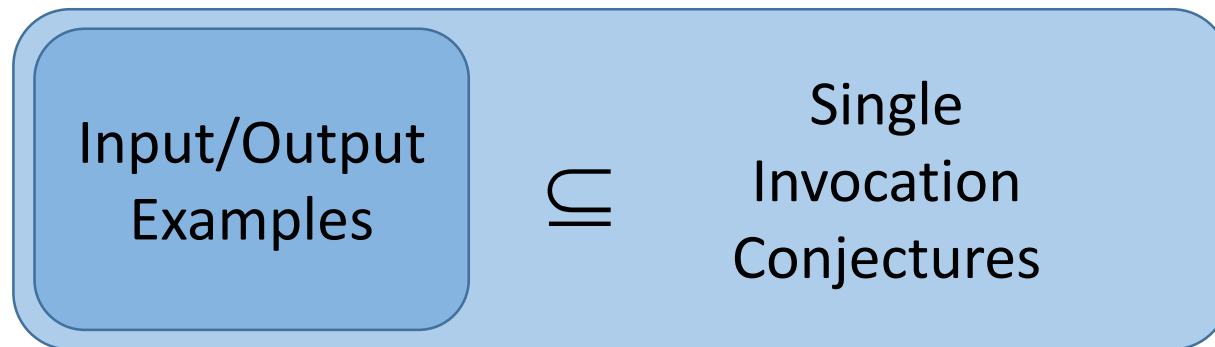
Find solutions  $f = \lambda x . t$ , where  $t$  is generated by grammar  $\mathcal{R}$

# Synthesis Conjectures : Overview

Input/Output  
Examples

e.g.  $\exists f . \forall x . (x = \mathbf{i}_1 \Rightarrow f(x) = \mathbf{o}_1) \wedge (x = \mathbf{i}_2 \Rightarrow f(x) = \mathbf{o}_2) \wedge (x = \mathbf{i}_3 \Rightarrow f(x) = \mathbf{o}_3)$

# Synthesis Conjectures : Overview



e.g.  $\exists f . \forall x y . \mathbf{f}(x, y) \geq x \wedge \mathbf{f}(x, y) \geq y \wedge (\mathbf{f}(x, y) = x \vee \mathbf{f}(x, y) = y)$

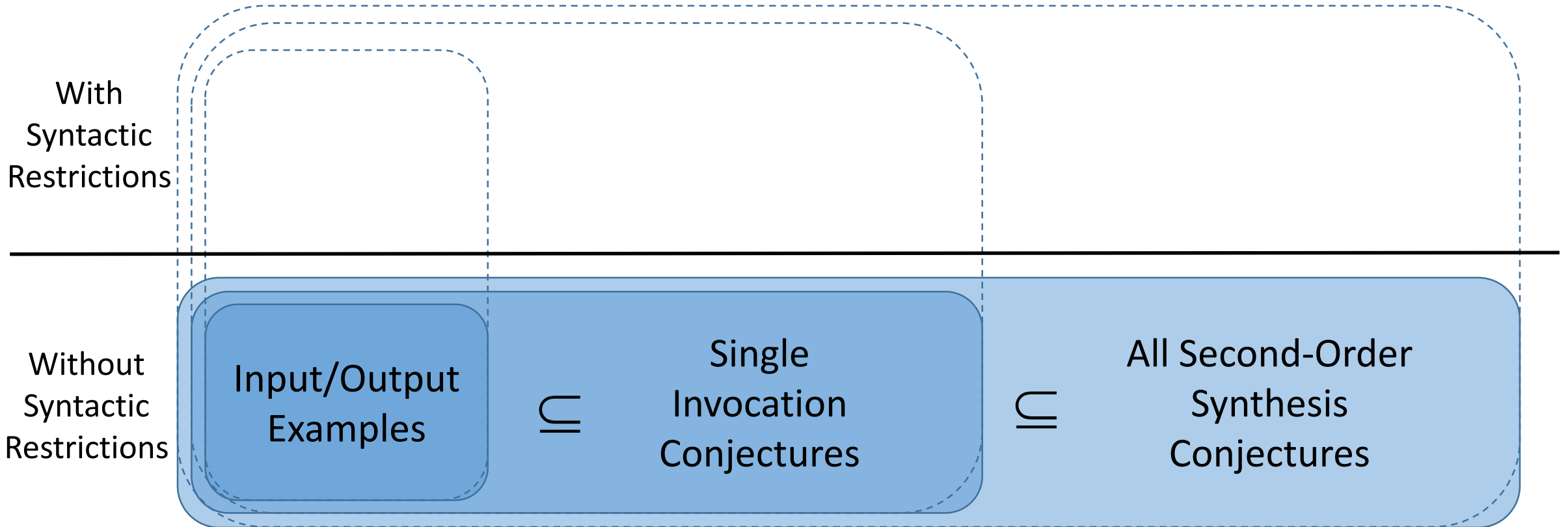
# Synthesis Conjectures : Overview



e.g.  $\exists f . \forall x y . f(x, y) = f(y, x)$



# Synthesis Conjectures : Overview



# Synthesis Conjectures : Overview

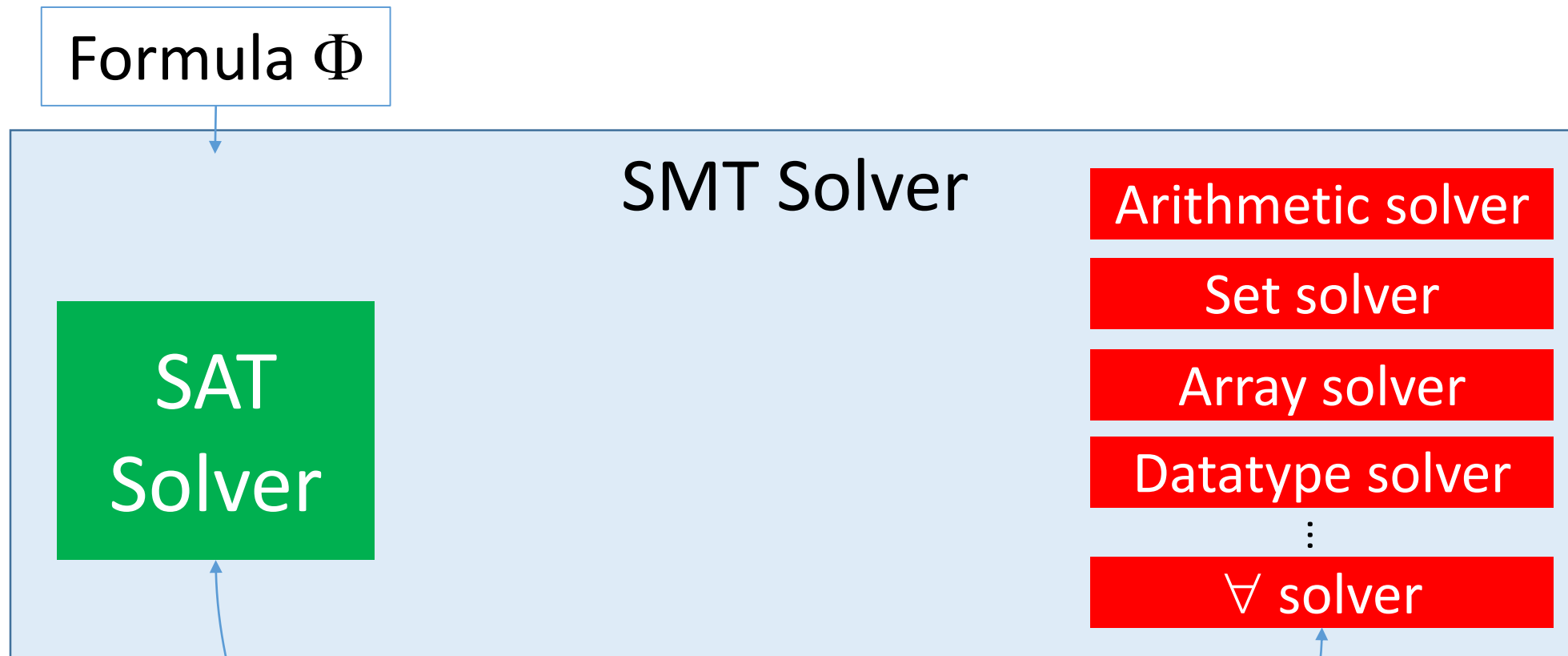
With Syntactic Restrictions	?	?	?
Without Syntactic Restrictions	?	?	?
	Input/Output Examples	Single Invocation Conjectures	Other Second-Order Synthesis Conjectures

# Synthesis Conjectures : Overview

With Syntactic Restrictions	?	?	?
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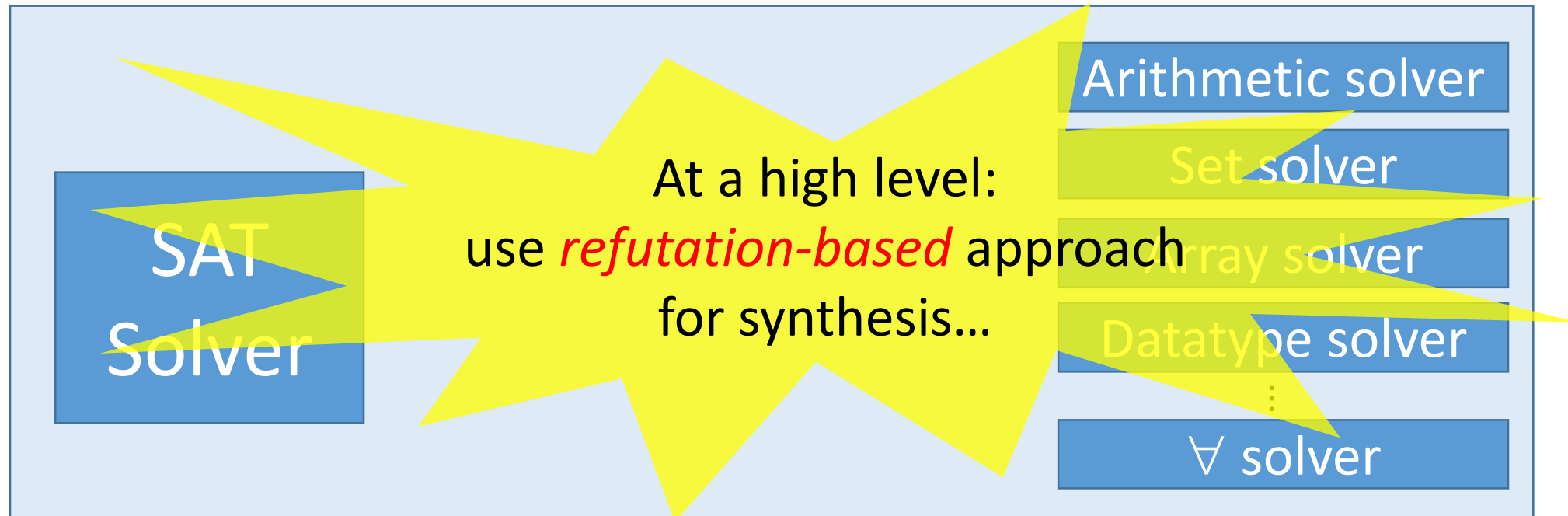
Will show how **DPLL(T)-based SMT solvers** can handle each class of conjecture

# DPLL(T)-based SMT Solvers



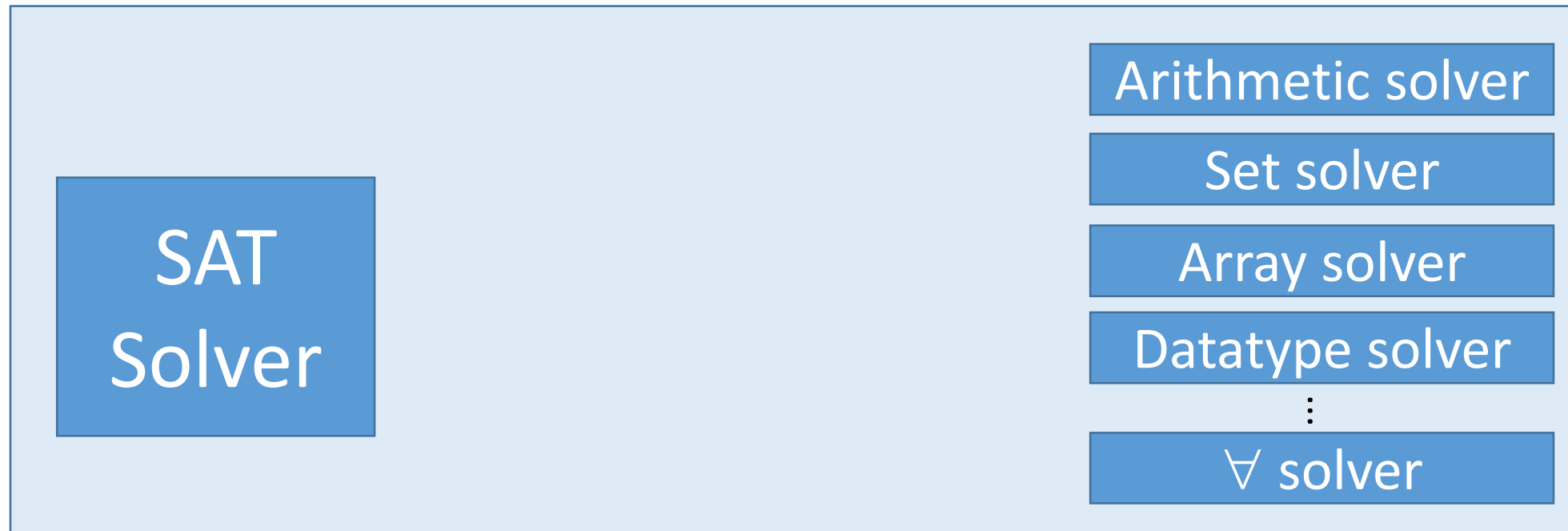
- Determines satisfiability of  $\Phi$  via combination of:  
*SAT solver* and *dedicated theory solvers*

# DPLL(T)-based SMT Solvers for Synthesis



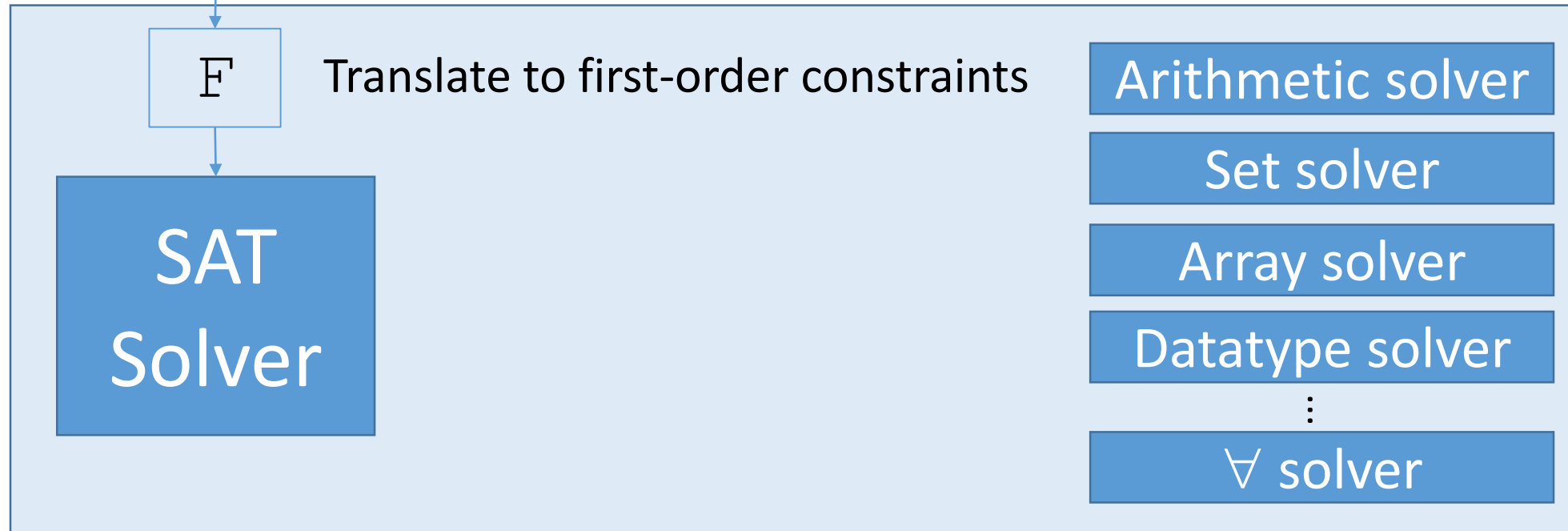
# DPLL(T)-based SMT Solvers for Synthesis

$\neg \exists f . \forall x . P(f, x)$  +  $\left[ \mathcal{R} \right]$  } *Negated* Synthesis Conjecture  
(+ syntactic restrictions  $\mathcal{R}$ )



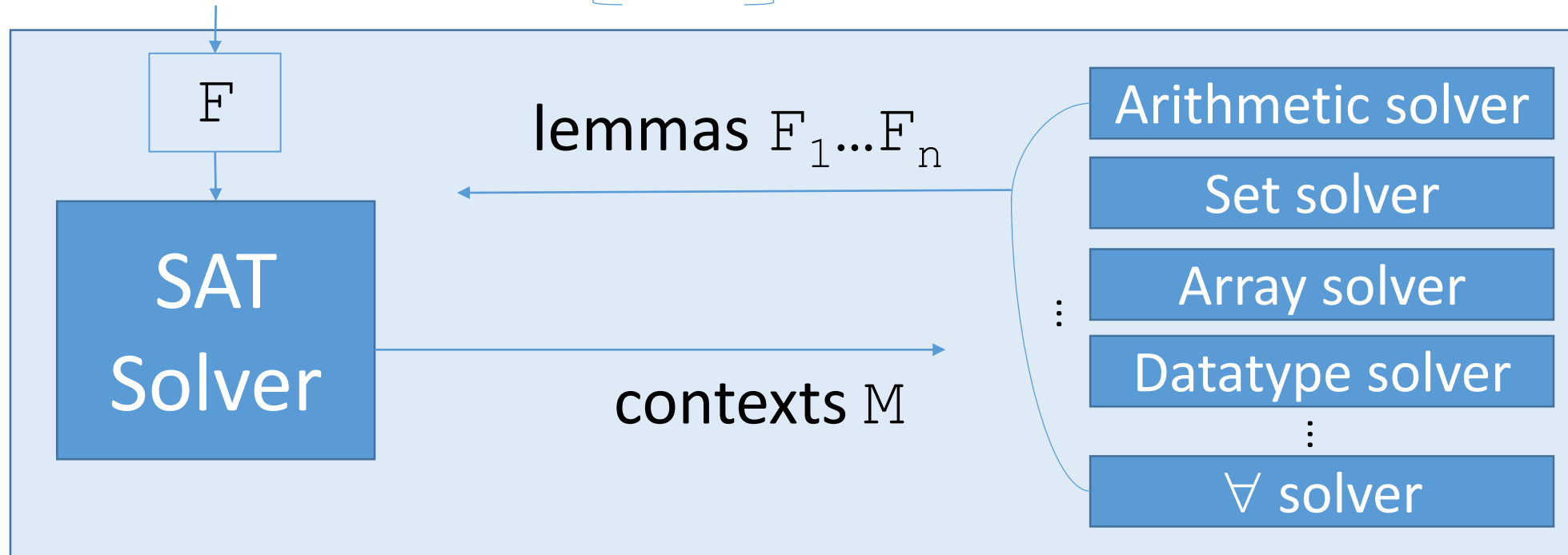
# DPLL(T)-based SMT Solvers for Synthesis

$$\neg \exists f . \forall x . P(f, x) + \left[ \mathcal{R} \right]$$



# DPLL(T)-based SMT Solvers for Synthesis

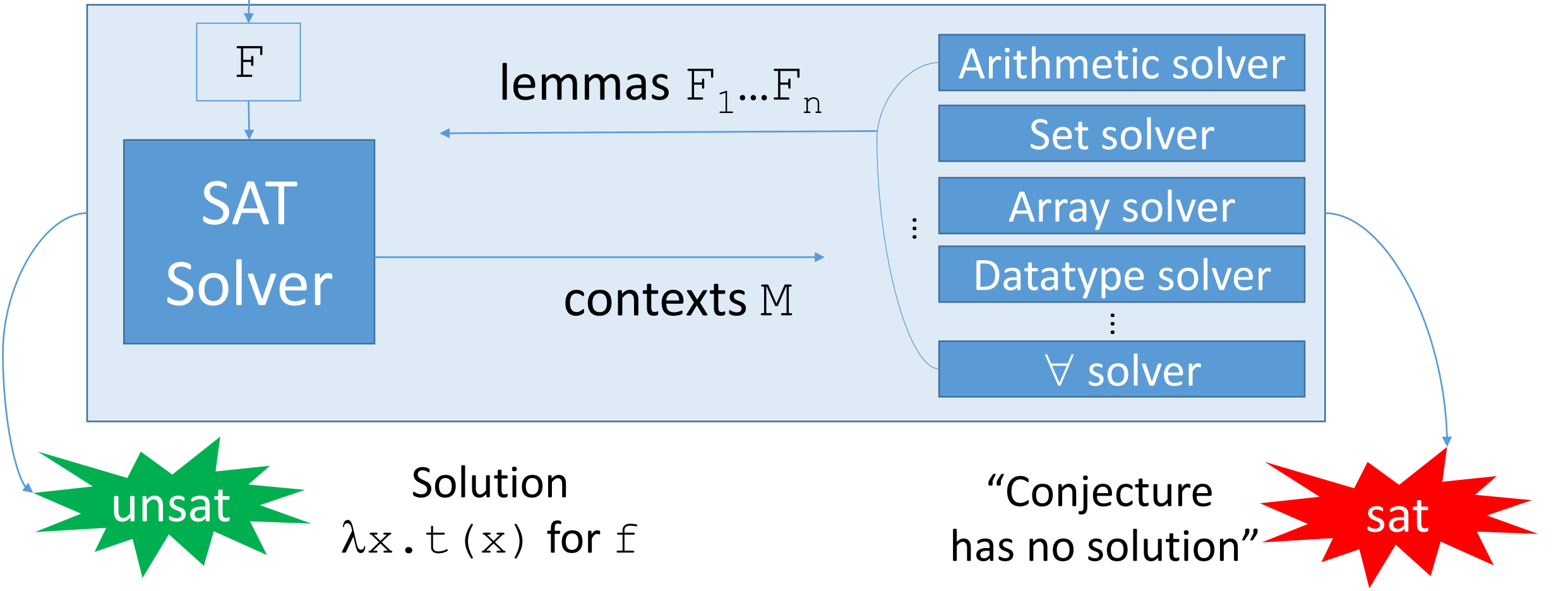
$$\neg \exists f. \forall x. P(f, x) + \left[ \mathcal{R} \right]$$






# DPLL(T)-based SMT Solvers for Synthesis

$$\neg \exists f. \forall x. P(f, x) + \left[ \mathcal{R} \right]$$



# Single Invocation w/o Syntactic Restrictions

With Syntactic Restrictions	?	?	?
Without Syntactic Restrictions	?		?
	Input/Output Examples	Single Invocation Conjectures	Other Second-Order Synthesis Conjectures

# Single Invocation w/o Syntactic Restrictions

- Some synthesis conjectures are *essentially first-order*

# Single Invocation w/o Syntactic Restrictions

- Some synthesis conjectures are *essentially first-order*:

$$\neg \exists f . \forall x y . \mathbf{f}(x, y) \geq x \wedge \mathbf{f}(x, y) \geq y \wedge (\mathbf{f}(x, y) = x \vee \mathbf{f}(x, y) = y)$$

“ $\mathbf{f}(x, y)$  is the maximum of  $x$  and  $y$ ”

# Single Invocation w/o Syntactic Restrictions

$$\neg \exists f . \forall x y . \underline{f(x, y)} \geq x \wedge \underline{f(x, y)} \geq y \wedge (\underline{f(x, y)} = x \vee \underline{f(x, y)} = y)$$

Int × Int → Int

All occurrence of  $f$  are in terms of the form  $f(x, y)$   
⇒ “single invocation” synthesis conjectures

# Single Invocation w/o Syntactic Restrictions

$$\neg \exists f . \forall x y . f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$$

$\text{Int} \times \text{Int} \rightarrow \text{Int}$

# Single Invocation w/o Syntactic Restrictions

$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$

Int  $\times$  Int  $\rightarrow$  Int

*Anti-skolemize*

$\neg \forall x y. \exists z. z \geq x \wedge z \geq y \wedge (z = x \vee z = y)$

Int

# Single Invocation w/o Syntactic Restrictions

$$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$$

Int × Int → Int

$$\neg \forall x y. \exists z. z \geq x \wedge z \geq y \wedge (z = x \vee z = y)$$

Int

“for each  $x, y$ , there exists a return value  $z$  that is the maximum of  $x$  and  $y$ ”



# Single Invocation w/o Syntactic Restrictions

$$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$$

Int × Int → Int

$$\neg \forall x y. \exists z. z \geq x \wedge z \geq y \wedge (z = x \vee z = y)$$

Int

*Simplify*

$$\exists x y. \forall z. \neg (z \geq x \wedge z \geq y \wedge (z = x \vee z = y))$$

# Single Invocation w/o Syntactic Restrictions

$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$

Int  $\times$  Int  $\rightarrow$  Int

$\neg \forall x y. \exists z. z \geq x \wedge z \geq y \wedge (z = x \vee z = y)$

Int

$\exists x y. \forall z. \neg (z \geq x \wedge z \geq y \wedge (z = x \vee z = y))$

*First-order linear arithmetic  $\Rightarrow$  Solvable by first-order  $\forall$ -instantiation*  
[Reynolds et al CAV2015]

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$

SAT Solver

LIA solver

Set solver

Array solver

Datatype solver

⋮

$\forall$  solver

# Single Invocation Synthesis in SMT

$$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$$

Translate to first-order

$$\forall z. \neg (z \geq x \wedge z \geq y \wedge (z = x \vee z = y))$$

SAT Solver

LIA solver

Set solver

Array solver

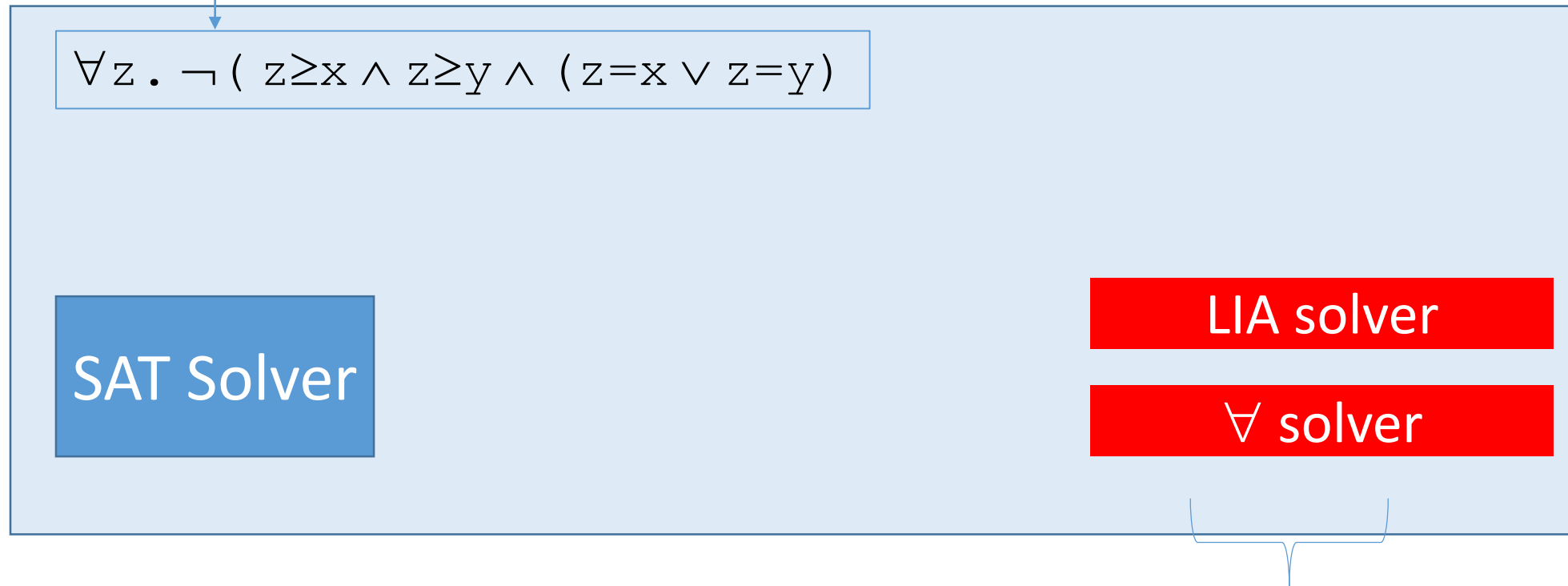
Datatype solver

⋮

$\forall$  solver

# Single Invocation Synthesis in SMT

$$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$$



Solve use first-order  $\forall$ -instantiation for linear arithmetic (LIA)

# Single Invocation Synthesis in SMT

$$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$$

$$\forall z. \neg (z \geq x \wedge z \geq y \wedge (z = x \vee z = y))$$

SAT Solver

LIA solver

$\forall$  solver

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$

SAT Solver

LIA solver

$\forall$  solver

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(\mathbf{x}, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(\mathbf{y}, x, y)$

SAT Solver

Instantiate  $z \rightarrow \mathbf{x}, z \rightarrow \mathbf{y}$

LIA solver

$\forall$  solver



# Single Invocation Synthesis in SMT

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow x < y$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow y < x$

SAT Solver

Simplify

LIA solver

$\forall$  solver

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow x < y$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow y < x \dots$

SAT Solver

LIA solver

$\forall$  solver

unsat

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow x < y$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow y < x$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\Rightarrow$  Solution for  $f$  can be constructed from unsatisfiable core of instantiations

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(x, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(y, x, y)$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda x y. ?$

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall xy. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(x, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(y, x, y)$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite}(\text{isMax}(x, x, y), x, ?)$

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall xy. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(x, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(\mathbf{y}, x, y)$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite}(\text{isMax}(x, x, y), x, \mathbf{y})$

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall xy. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(x, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(y, x, y)$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite}((x \geq x \wedge x \geq y \wedge (x = x \vee x = y)), x, y)$

$\Rightarrow$  Expand

# Single Invocation Synthesis in SMT

$\neg \exists f. \forall xy. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(x, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(y, x, y)$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite}(x \geq y, x, y)$

$\Rightarrow$  Simplify



# Single Invocation Synthesis in SMT

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$

$\forall z. \neg \text{isMax}(z, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(x, x, y)$   
 $\forall z. \neg \text{isMax}(z, x, y) \Rightarrow \neg \text{isMax}(y, x, y)$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda x y. \text{ite}(x \geq y, x, y)$

*Desired function*

# Single Invocation Synthesis in SMT

- Requires: method for selecting a term  $?t$  for instantiation

$$\begin{aligned} & (\forall z . \neg ( z \geq x \wedge z \geq y \wedge ( z = x \vee z = y ) ) \Rightarrow \\ & \neg ( ?t \geq x \wedge ?t \geq y \wedge ( ?t = x \vee ?t = y ) ) \end{aligned}$$

$\forall$  solver

# Single Invocation Synthesis in SMT

- Requires: method for selecting a term **?t** for instantiation
  - Use *counterexample-guided quantifier instantiation* (CEGQI)
  - General idea used in a number of related works:

[Monniaux 2010, Komuravelli et al 2014, Reynolds et al 2015, Dutertre 2015, Bjorner/Janota 2016, Fediyukovich et al 2016, Preiner et al 2017]

$$\begin{aligned} & (\forall z . \neg ( z \geq x \wedge z \geq y \wedge ( z = x \vee z = y ) ) \Rightarrow \\ & \neg ( \text{?t} \geq x \wedge \text{?t} \geq y \wedge ( \text{?t} = x \vee \text{?t} = y ) ) \end{aligned}$$

$\forall$  solver

# Counterexample-Guided $\forall$ -Instantiation

$$\begin{aligned} & (\forall \mathbf{z} . \neg ( \mathbf{z} \geq x \wedge \mathbf{z} \geq y \wedge ( \mathbf{z} = x \vee \mathbf{z} = y ) ) ) \Rightarrow \\ & \neg ( ?t \geq x \wedge ?t \geq y \wedge ( ?t = x \vee ?t = y ) ) \end{aligned}$$

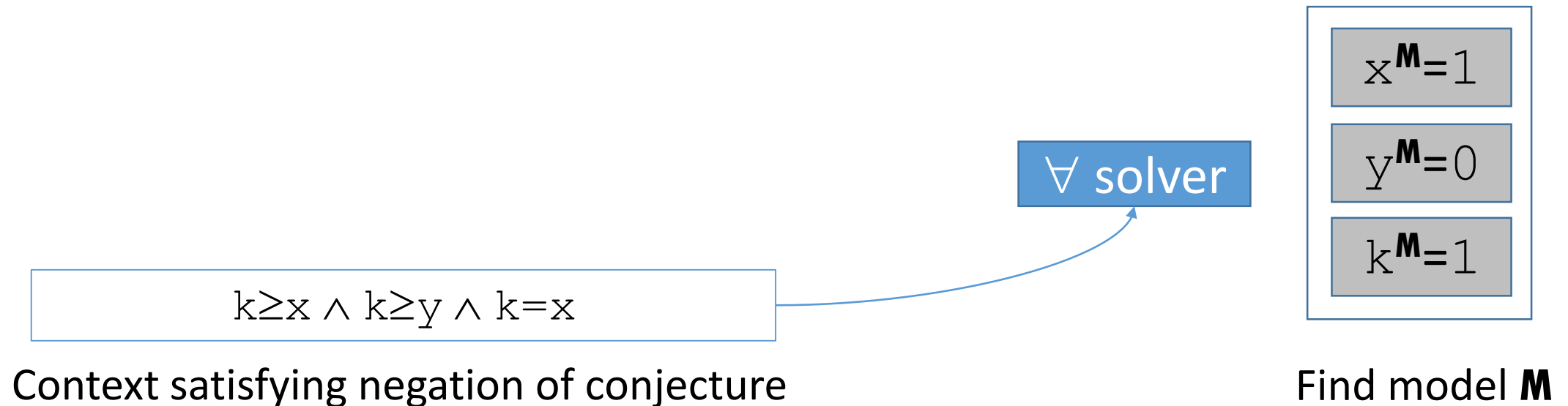
$\forall$  solver

$$\exists \mathbf{k} . \mathbf{k} \geq x \wedge \mathbf{k} \geq y \wedge ( \mathbf{k} = x \vee \mathbf{k} = y )$$

$\Rightarrow$  Consider conjecture's negation

- $\mathbf{k}$  is counterexample to conjecture

# Counterexample-Guided $\forall$ -Instantiation



# Counterexample-Guided $\forall$ -Instantiation

Consider lower bounds for  $k$

$$k \geq x \wedge k \geq y \wedge k = x$$

$\forall$  solver

$$x^M = 1$$

$$y^M = 0$$

$$k^M = 1$$

# Counterexample-Guided $\forall$ -Instantiation

**x** is the  
*maximal lower*  
*bound* for  $k$   
in  $\mathbf{M}$

$\forall$  solver

$$k \geq \mathbf{x} \wedge k \geq y \wedge k = x$$

$$\mathbf{x}^{\mathbf{M}} = 1$$

$$y^{\mathbf{M}} = 0$$

$$k^{\mathbf{M}} = 1$$

# Counterexample-Guided $\forall$ -Instantiation

Select instantiation

$z \rightarrow \mathbf{x}$

$(\forall z. \neg (z \geq x \wedge z \geq y \wedge (z = x \vee z = y))) \Rightarrow$   
 $\neg (\mathbf{x} \geq x \wedge \mathbf{x} \geq y \wedge (\mathbf{x} = x \vee \mathbf{x} = y))$

$k \geq x \wedge k \geq y \wedge k = x$

$\forall$  solver

$x^M = 1$

$y^M = 0$

$k^M = 1$



# Counterexample-Guided $\forall$ -Instantiation

Requires a *selection function*  $(M, \mathbf{M}, k) \rightarrow x$

Select instantiation

$z \rightarrow x$

$(\forall z. \neg (z \geq x \wedge z \geq y \wedge (z = x \vee z = y))) \Rightarrow$   
 $\neg (x \geq x \wedge x \geq y \wedge (x = x \vee x = y))$

$\forall$  solver

$k \geq x \wedge k \geq y \wedge k = x$

$x^M = 1$

$y^M = 0$

$k^M = 1$

# Counterexample-Guided $\forall$ -Instantiation

Quantifier Elimination Procedures

$\Leftarrow(\Rightarrow)?$

Instantiation-Based procedures for  $\exists\forall$  formulas

$\Leftrightarrow$

Synthesis procedures for single-invocation properties

# CEGQI Selection Functions

- Can devise **CEGQI selection functions** for:

- Linear real arithmetic (LRA)

- Maximal lower (minimal upper) bounds

Analogous to [\[Loos+Wiespfenning 93\]](#)

- Interior point method:

Analogous to [\[Ferrante+Rackoff 79\]](#)

- Linear integer arithmetic (LIA)

- Maximal lower (minimal upper) bounds (+c)

Analogous to [\[Cooper 72\]](#)

- Bitvectors/finite domains

- (Naively) Value instantiations

- Datatypes, ...

$$l_1 < k, \dots, l_n < k \rightarrow \{x \rightarrow l_{\max} + \delta\}$$

*...may involve virtual terms  $\delta, \infty$*

$$l_{\max} < k < u_{\min} \rightarrow \{x \rightarrow (l_{\max} + u_{\min}) / 2\}$$

$$l_1 < k, \dots, l_n < k \rightarrow \{x \rightarrow l_{\max} + c\}$$

$$\dots \rightarrow \{x \rightarrow k^M\}$$

# CEGQI Selection Functions

- Can devise **CEGQI selection functions** for:

- Linear real arithmetic (LRA)

- Maximal lower (minimal upper) bounds

Analogous to [Loos+Wiespfenning 93]

$$l_1 < k, \dots, l_n < k \rightarrow \{x \rightarrow l_{\max} + \delta\}$$

*...may involve virtual terms  $\delta, \infty$*

- Interior point method:

Analogous to [Ferrante+Rackoff 79]

$$l_{\max} < k < u_{\min} \rightarrow \{x \rightarrow (l_{\max} + u_{\min}) / 2\}$$

- Linear integer arithmetic (LIA)

- Maximal lower (minimal upper) bounds (+c)

Analogous to [Cooper 72]

$$l_1 < k, \dots, l_n < k \rightarrow \{x \rightarrow l_{\max} + c\}$$

- Bitvectors/finite domains

- (Naively) Value instantiations

$$\dots \rightarrow \{x \rightarrow k^M\}$$

- Datatypes, ...

$\Rightarrow$  Each gives a **sound** and **complete** procedure for single invocation conjectures

# CEGQI in CVC4



- Highly efficient:
  - for synthesis:
    - CVC4 won SyGuS-Comp GENERAL track 2015, CLIA track 2015-2016
  - and also for automated theorem proving:
    - CVC4 won TFA division of CASC 2014, LIA/LRA divisions of SMT COMP 2014-2016
- Applicable to multiple-function conjectures  $\exists f g . \forall x . P ( f ( x ) , g ( x ) , x )$
- **Sound** and **complete** for single invocation conjectures over LIA, LRA  
See [\[Reynolds/King/Kuncak FMSD2017, to appear\]](#)
- **Disadvantage**: leads to verbose solutions

# Overview

With Syntactic Restrictions	?	?	?
Without Syntactic Restrictions	?	Counterexample Guided $\forall$ -Instantiation	?
Input/Output Examples	Single Invocation Conjectures	Other Second-Order Synthesis Conjectures	

# What if we apply CEGQI to I/O Examples?

$\neg \exists f . \forall x . (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

SAT Solver

LIA solver

$\forall$  solver

# What if we apply CEGQI to I/O Examples?

$\neg \exists f . \forall x . (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

$\forall z . \neg ((x=1 \Rightarrow z=0) \wedge (x=2 \Rightarrow z=1) \wedge (x=3 \Rightarrow z=2))$

SAT Solver

LIA solver

$\forall$  solver



# What if we apply CEGQI to I/O Examples?

$\neg \exists f . \forall x . (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

$\forall z . \neg ( (x=1 \Rightarrow z=0) \wedge (x=2 \Rightarrow z=1) \wedge (x=3 \Rightarrow z=2) )$   
 $(\forall z \dots) \Rightarrow \neg ( (x=1 \Rightarrow 0=0) \wedge (x=2 \Rightarrow 0=1) \wedge (x=3 \Rightarrow 0=2) )$   
 $(\forall z \dots) \Rightarrow \neg ( (x=1 \Rightarrow 1=0) \wedge (x=2 \Rightarrow 1=1) \wedge (x=3 \Rightarrow 1=2) )$   
 $(\forall z \dots) \Rightarrow \neg ( (x=1 \Rightarrow 2=0) \wedge (x=2 \Rightarrow 2=1) \wedge (x=3 \Rightarrow 2=2) )$

SAT Solver

Instantiate

$z \rightarrow 0, z \rightarrow 1, z \rightarrow 2$

LIA solver

$\forall$  solver

# What if we apply CEGQI to I/O Examples?

$\neg \exists f . \forall x . (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

$\forall z . \neg ( (x=1 \Rightarrow z=0) \wedge (x=2 \Rightarrow z=1) \wedge (x=3 \Rightarrow z=2) )$   
 $(\forall z \dots) \Rightarrow (x=2 \vee x=3)$   
 $(\forall z \dots) \Rightarrow (x=1 \vee x=3)$   
 $(\forall z \dots) \Rightarrow (x=1 \vee x=2)$

(simplify)

SAT Solver

LIA solver

$\forall$  solver

# What if we apply CEGQI to I/O Examples?

$\neg \exists f . \forall x . (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

$\forall z . \neg ( (x=1 \Rightarrow z=0) \wedge (x=2 \Rightarrow z=1) \wedge (x=3 \Rightarrow z=2) )$   
 $(\forall z \dots) \Rightarrow (x=2 \vee x=3)$   
 $(\forall z \dots) \Rightarrow (x=1 \vee x=3)$   
 $(\forall z \dots) \Rightarrow (x=1 \vee x=2) \dots$

SAT Solver

LIA solver

$\forall$  solver

unsat

# What if we apply CEGQI to I/O Examples?

$\neg \exists f. \forall x. (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

$\forall z. \neg ((x=1 \Rightarrow z=0) \wedge (x=2 \Rightarrow z=1) \wedge (x=3 \Rightarrow z=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 0=0) \wedge (x=2 \Rightarrow 0=1) \wedge (x=3 \Rightarrow 0=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 1=0) \wedge (x=2 \Rightarrow 1=1) \wedge (x=3 \Rightarrow 1=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 2=0) \wedge (x=2 \Rightarrow 2=1) \wedge (x=3 \Rightarrow 2=2))$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite} ( \begin{array}{l} x=1 \Rightarrow 0=0 \wedge \\ x=2 \Rightarrow 0=1 \wedge \\ x=3 \Rightarrow 0=2 \end{array} , 0, \dots )$

# What if we apply CEGQI to I/O Examples?

$\neg \exists f. \forall x. (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

$\forall z. \neg ((x=1 \Rightarrow z=0) \wedge (x=2 \Rightarrow z=1) \wedge (x=3 \Rightarrow z=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 0=0) \wedge (x=2 \Rightarrow 0=1) \wedge (x=3 \Rightarrow 0=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow \mathbf{1}=0) \wedge (x=2 \Rightarrow \mathbf{1}=1) \wedge (x=3 \Rightarrow \mathbf{1}=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 2=0) \wedge (x=2 \Rightarrow 2=1) \wedge (x=3 \Rightarrow 2=2))$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite} ( \begin{array}{l} x=1 \Rightarrow 0=0 \wedge \\ x=2 \Rightarrow 0=1 \wedge \\ x=3 \Rightarrow 0=2 \end{array} , 0, \begin{array}{l} x=1 \Rightarrow \mathbf{1}=0 \wedge \\ x=2 \Rightarrow \mathbf{1}=1 \wedge \\ x=3 \Rightarrow \mathbf{1}=2 \end{array} , \mathbf{1}, \dots )$

# What if we apply CEGQI to I/O Examples?

$\neg \exists f. \forall x. (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

$\forall z. \neg ((x=1 \Rightarrow z=0) \wedge (x=2 \Rightarrow z=1) \wedge (x=3 \Rightarrow z=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 0=0) \wedge (x=2 \Rightarrow 0=1) \wedge (x=3 \Rightarrow 0=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 1=0) \wedge (x=2 \Rightarrow 1=1) \wedge (x=3 \Rightarrow 1=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 2=0) \wedge (x=2 \Rightarrow 2=1) \wedge (x=3 \Rightarrow 2=2))$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite} ( \begin{array}{l} x=1 \Rightarrow 0=0 \wedge \\ x=2 \Rightarrow 0=1 \wedge \\ x=3 \Rightarrow 0=2 \end{array} , 0, \begin{array}{l} x=1 \Rightarrow 1=0 \wedge \\ x=2 \Rightarrow 1=1 \wedge \\ x=3 \Rightarrow 1=2 \end{array} , 1, 2 )$

# What if we apply CEGQI to I/O Examples?

$\neg \exists f. \forall x. (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

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 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 1=0) \wedge (x=2 \Rightarrow 1=1) \wedge (x=3 \Rightarrow 1=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 2=0) \wedge (x=2 \Rightarrow 2=1) \wedge (x=3 \Rightarrow 2=2))$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite}(x=1, 0, x=2, 1, 2)$

$\Rightarrow$  simplify

# What if we apply CEGQI to I/O Examples?

$\neg \exists f. \forall x. (x=1 \Rightarrow f(x)=0) \wedge (x=2 \Rightarrow f(x)=1) \wedge (x=3 \Rightarrow f(x)=2)$

$\forall z. \neg ((x=1 \Rightarrow z=0) \wedge (x=2 \Rightarrow z=1) \wedge (x=3 \Rightarrow z=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 0=0) \wedge (x=2 \Rightarrow 0=1) \wedge (x=3 \Rightarrow 0=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 1=0) \wedge (x=2 \Rightarrow 1=1) \wedge (x=3 \Rightarrow 1=2))$   
 $(\forall z \dots) \Rightarrow \neg ((x=1 \Rightarrow 2=0) \wedge (x=2 \Rightarrow 2=1) \wedge (x=3 \Rightarrow 2=2))$

SAT Solver

LIA solver

$\forall$  solver

unsat

$\lambda xy. \text{ite}(x=1, 0, x=2, 1, 2)$

$\Rightarrow$  Produces **trivial solution**  
(input/output table)



# Overview

With Syntactic Restrictions	? ...although prefer solutions here	?	?
Without Syntactic Restrictions	CEGQI (trivially)	Counterexample Guided $\forall$ -Instantiation	?
	Input/Output Examples	Single Invocation Conjectures	Other Second-Order Synthesis Conjectures

# What if there are syntactic restrictions?

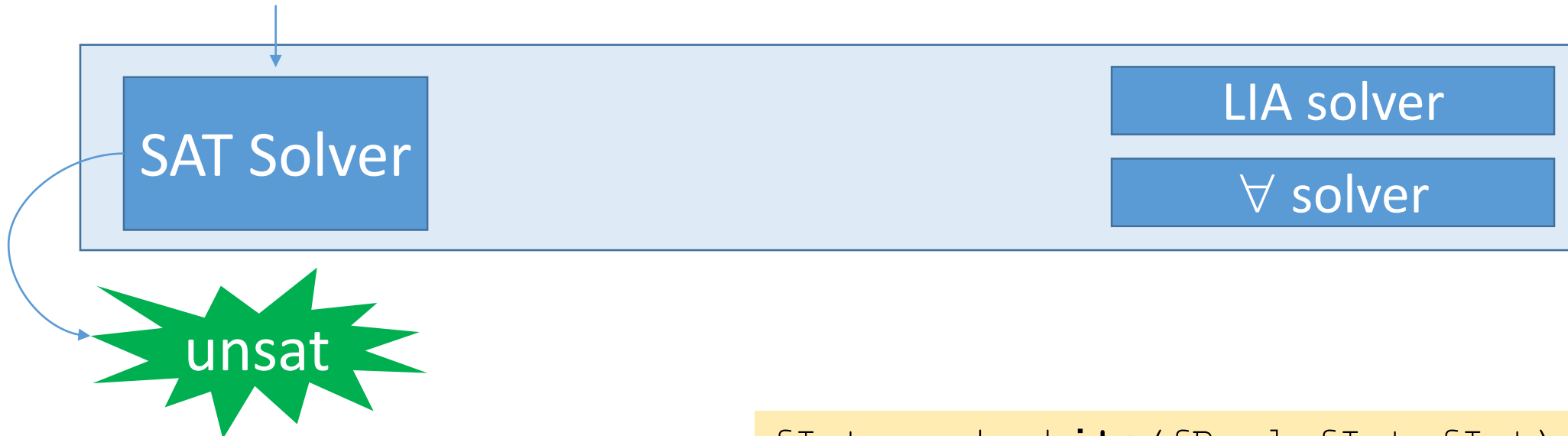
```
¬∃f.∀xy.isMax(f(x,y),x,y)
```

where solution meets syntactic restrictions  $\mathcal{R}$  :

```
 $\mathcal{R}$  : fInt := x | y | ite(fBool, fInt, fInt)  
fBool := >(fInt, fInt) | =(fInt, fInt) | ¬(fBool)
```

# What if there are syntactic restrictions?

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$



$f = \lambda x y. \text{ite}(x \geq y, x, y)$

$\mathcal{R} :$   $f\text{Int} := \mathbf{x} \mid \mathbf{y} \mid \mathbf{ite}(f\text{Bool}, f\text{Int}, f\text{Int})$   
 $f\text{Bool} := \mathbf{>}(f\text{Int}, f\text{Int}) \mid \mathbf{=}(f\text{Int}, f\text{Int}) \mid \mathbf{\neg}(f\text{Bool})$

# What if there are syntactic restrictions?

$\neg \exists f. \forall x y. \text{isMax}(f(x, y), x, y)$

SAT Solver

LIA solver

$\forall$  solver

unsat

$f = \lambda x y. \text{ite}(x \geq y, x, y)$

$\mathcal{R} :$

$f\text{Int} := \mathbf{x} \mid \mathbf{y} \mid \mathbf{ite}(f\text{Bool}, f\text{Int}, f\text{Int})$   
 $f\text{Bool} := \mathbf{>}(f\text{Int}, f\text{Int}) \mid \mathbf{=}(f\text{Int}, f\text{Int}) \mid \mathbf{\neg}(f\text{Bool})$

Solution Reconstruction

[Reynolds et al CAV2015]

$f = \lambda x y. \text{ite}(\neg y < x, x, y)$

fail

$\Rightarrow$  Highly heuristic

# Overview

With Syntactic Restrictions	?	?	?
Without Syntactic Restrictions	CEGQI (trivially)	Counterexample Guided $\forall$ -Instantiation	?
	Input/Output Examples	Single Invocation Conjectures	Other Second-Order Synthesis Conjectures

# For Everything Else...

With Syntactic Restrictions	Enumerative SyGuS	Enumerative SyGuS CEGQI + reconstruction	Enumerative SyGuS
Without Syntactic Restrictions	CEGQI (trivially)	Counterexample Guided $\forall$ -Instantiation	Enumerative SyGuS (using default restrictions)
Input/Output Examples	Single Invocation Conjectures	Other Second-Order Synthesis Conjectures	

# Enumerative Syntax-Guided Synthesis

## Conjecture

$\exists f. \forall x. P(f, x)$

Test



```
0
1
x
1+1
x+1
x+x
ite(x>0,0,1)
.
.
.
```

Enumerate



## Syntactic Restrictions $\mathcal{R}$

```
fInt := x | 0 | 1 | +(fInt, fInt) |
      ite(fBool, fInt, fInt)
fBool := >(fInt, fInt) | =(fInt, fInt) |
        ¬(fBool)
```

- Idea: enumerate terms generated by the grammar
- Approach used by number of synthesis solvers [[Solar-Lezama 2013](#), [Udupa et al 2013](#)]

# Enumerative Syntax-Guided Synthesis **in SMT**

Conjecture

$\exists f. \forall x. P(f, x)$

Test

```
0
1
x
1+1
x+1
x+x
ite(x>0, 0, 1)
.
.
.
```

Enumerate

Syntactic  
Restrictions  $\mathcal{R}$

```
fInt := x | 0 | 1 | +(fInt, fInt) |
      ite(fBool, fInt, fInt)
fBool := >(fInt, fInt) | =(fInt, fInt) |
        ¬(fBool)
```

- Idea: enumerate terms generated by the grammar
  - Approach used by number of synthesis solvers [[Solar-Lezama 2013](#), [Udupa et al 2013](#)]
- ⇒ **In this talk:** how this approach can be integrated in a DPLL(T) SMT solver



# Enumerative Syntax-Guided Synthesis in SMT

## Conjecture

$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$

$\mathcal{R}$ :

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | ≤(fInt, fInt)  
       =(fInt, fInt)
```

# Enumerative Syntax-Guided Synthesis in SMT

## Conjecture

$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$

$\mathcal{R}$ :

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | ≤(fInt, fInt)  
       =(fInt, fInt)
```

Construct *inductive datatypes* I, B corresponding to  $\mathcal{R}$

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)  
B := ≥(I, I) | =(I, I)
```

$\Rightarrow$  Involves flattening, minimization

# Enumerative Syntax-Guided Synthesis in SMT

## Conjecture

$$\exists \mathbf{f}. \forall x y. \mathbf{f}(x, y) \geq x \wedge \mathbf{f}(x, y) = \mathbf{f}(y, x)$$

Int × Int → Int

Encode conjecture using *deep embedding* involving  $\mathbb{I}$

$$\exists \mathbf{d}. \forall x y. \mathbf{E}(\mathbf{d}, x, y) \geq x \wedge \mathbf{E}(\mathbf{d}, x, y) = \mathbf{E}(\mathbf{d}, y, x)$$

$\mathbb{I}$

$\mathcal{R}$ :

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | ≤(fInt, fInt)  
        =(fInt, fInt)
```

```
 $\mathbb{I}$  := x | y | 0 | 1 | +( $\mathbb{I}$ ,  $\mathbb{I}$ ) | ite(B,  $\mathbb{I}$ ,  $\mathbb{I}$ )  
B := ≥( $\mathbb{I}$ ,  $\mathbb{I}$ ) | =( $\mathbb{I}$ ,  $\mathbb{I}$ )
```

# Enumerative Syntax-Guided Synthesis in SMT

## Conjecture

$$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$$
$$\exists d. \forall x y. \mathbf{E}(d, x, y) \geq x \wedge \mathbf{E}(d, x, y) = \mathbf{E}(d, y, x)$$

$\mathbf{E}: I \times \text{Int} \times \text{Int} \rightarrow \text{Int}$

$\mathcal{R}$ :

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | ≤(fInt, fInt)  
       =(fInt, fInt)
```

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)  
B := ≥(I, I) | =(I, I)
```

- $\mathbf{E}(d, x, y)$  *evaluates*  $d$  for arguments  $x, y$ , e.g.  $\mathbf{E}(+(x, y), 2, 3) = 5$

# Enumerative Syntax-Guided Synthesis in SMT

Conjecture

$$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$$

$\mathcal{R}$ :

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | ≤(fInt, fInt)  
        =(fInt, fInt)
```

$$\exists d. \forall x y. E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x)$$

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)  
B := ≥(I, I) | =(I, I)
```

$\Rightarrow$  Solvable by combination of datatypes, LIA, UF, and  $\forall$ -instantiation

# Enumerative Syntax-Guided Synthesis in SMT

$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$

SAT Solver

LIA solver

UF solver

Array solver

Datatype solver

⋮

$\forall$  solver

# Enumerative Syntax-Guided Synthesis in SMT

$$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$$

$$\exists d. \forall x y. (E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x))$$

SAT Solver

LIA solver

UF solver

Array solver

Datatype solver

⋮

$\forall$  solver

# Enumerative Syntax-Guided Synthesis in SMT

$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$

$\exists d. \forall x y. (E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x))$

SAT Solver

LIA solver

UF solver

Datatype solver

$\forall$  solver



# Enumerative Syntax-Guided Synthesis in SMT

$$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$$

$$\exists d. \forall x y. (E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x))$$

SAT Solver

*Will mostly focus on*

LIA solver

UF solver

Datatype solver

$\forall$  solver

# Enumerative Syntax-Guided Synthesis in SMT

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := ≥(I, I) | =(I, I) | ¬(B)
```

- **Idea:** Return candidate solutions based on value of  $d^M$  in models  $M$  where  $d$  is of type  $I$

SAT Solver

Datatype solver

# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\text{is}_x(d) \text{vis}_y(d) \text{vis}_0(d) \text{vis}_1(d) \text{vis}_+(d) \text{vis}_{\text{ite}}(d)$

Split on top symbol of  $d$

SAT Solver

Datatype solver

# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid + (I, I) \mid \text{ite} (B, I, I)$   
 $B ::= \geq (I, I) \mid = (I, I) \mid \neg (B)$

$is_x(d)$   $vis_y(d)$   $vis_0(d)$   $vis_1(d)$   $vis_+(d)$   $vis_{ite}(d)$

SAT Solver

Datatype solver

$is_x(d)$

Find satisfying context

# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\text{is}_x(d) \text{vis}_y(d) \text{vis}_0(d) \text{vis}_1(d) \text{vis}_+(d) \text{vis}_{\text{ite}}(d)$

SAT Solver

Datatype solver

$d^M = x$

$\text{is}_x(d)$

Find model **M**

# Enumerative Syntax-Guided Synthesis in SMT

$I := x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B := \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\text{is}_x(d) \text{vis}_y(d) \text{vis}_0(d) \text{vis}_1(d) \text{vis}_+(d) \text{vis}_{\text{ite}}(d)$   
 $\text{is}_x(d) \Rightarrow \forall xy. (E(x, x, y) \geq x \wedge E(x, x, y) = E(x, y, x))$

Check conjecture for  
candidate  $d \rightarrow x$

SAT Solver

Datatype solver

$\forall$  solver

$d^M = x$

# Enumerative Syntax-Guided Synthesis in SMT

```
I := x | y | 0 | 1 | + (I, I) | ite (B, I, I)
B := ≥ (I, I) | = (I, I) | ¬ (B)
```

```
is_x (d) vis_y (d) vis_0 (d) vis_1 (d) vis_+ (d) vis_ite (d)
is_x (d) ⇒ ∀xy. ( x ≥ x ∧ x = y)
```

Simplify

SAT Solver

Datatype solver

# Enumerative Syntax-Guided Synthesis in SMT

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := >=(I, I) | =(I, I) | ¬(B)
```

```
is_x(d) vis_y(d) vis_0(d) vis_1(d) vis_+(d) vis_ite(d)
¬is_x(d)
```

Simplify

SAT Solver

Datatype solver



# Enumerative Syntax-Guided Synthesis in SMT

$I := x \mid y \mid 0 \mid 1 \mid + (I, I) \mid \text{ite} (B, I, I)$   
 $B := \geq (I, I) \mid = (I, I) \mid \neg (B)$

$is_x(d) \vee \underline{is_y(d)} \vee vis_0(d) \vee vis_1(d) \vee vis_+(d) \vee vis_{ite}(d)$   
 $\underline{\neg is_x(d)}$

SAT Solver

Datatype solver

$\neg is_x(d)$   
 $is_y(d)$

Find next satisfying context

# Enumerative Syntax-Guided Synthesis in SMT

$I := x \mid y \mid 0 \mid 1 \mid + (I, I) \mid \text{ite} (B, I, I)$   
 $B := \geq (I, I) \mid = (I, I) \mid \neg (B)$

$\text{is}_x(d) \text{vis}_y(d) \text{vis}_0(d) \text{vis}_1(d) \text{vis}_+(d) \text{vis}_{\text{ite}}(d)$   
 $\neg \text{is}_x(d)$

SAT Solver

Datatype solver

$\neg \text{is}_x(d)$   
 $\text{is}_y(d)$

$d^M = y$

Find next model **M**

# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\text{is}_x(d) \text{vis}_y(d) \text{vis}_0(d) \text{vis}_1(d) \text{vis}_+(d) \text{vis}_{\text{ite}}(d)$   
 $\neg \text{is}_x(d)$   
 $\neg \text{is}_y(d)$

SAT Solver

Datatype solver

$\forall$  solver

$d^M = \dots$

# Enumerative Syntax-Guided Synthesis in SMT

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := >=(I, I) | =(I, I) | ¬(B)
```

```
is_x(d) vis_y(d) vis_0(d) vis_1(d) vis_+(d) vis_ite(d)
¬is_x(d)
¬is_y(d)
...
is_x(d.1) vis_y(d.1) vis_0(d.1) vis_1(d.1) vis_+(d.1) vis_ite(d.1)
is_x(d.2) vis_y(d.2) vis_0(d.2) vis_1(d.2) vis_+(d.2) vis_ite(d.2)
¬is_+(d) ∨ ¬is_x(d.1) ∨ ¬is_x(d.2)
...
```

SAT Solver

...and repeat

Datatype solver

∀ solver

$d^M = \dots$

# Enumerative Syntax-Guided Synthesis in SMT

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := ≥(I, I) | =(I, I) | ¬(B)
```

```
is_x(d)vis(d)vis(d)vis(d)vis(d)vis(d)
¬is_x(
...
```

## Optimization:

*Only consider terms  $d^M$  whose analog is unique up to theory-specific simplification ↓*

SAT Solver

Datatype solver

∀ solver

$d^M = \dots$

# Enumerative Syntax-Guided Synthesis in SMT

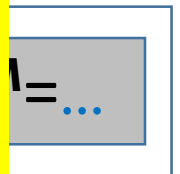
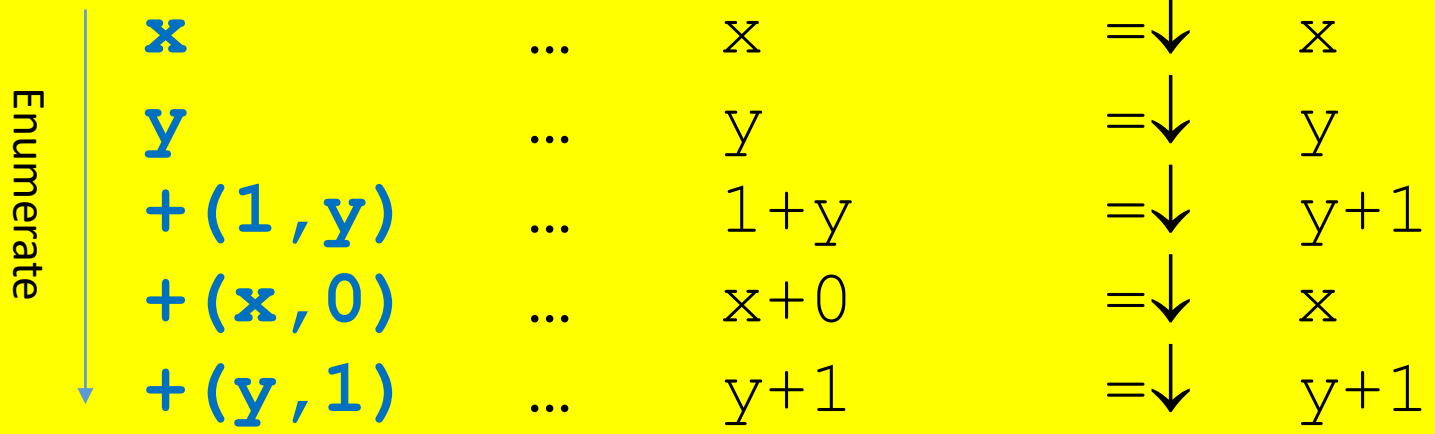
```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := >=(I, I) | =(I, I) | ¬(B)
```

```
is_x(d)vis_(d)vis_(d)vis_(d)vis_(d)vis_(d)
¬is_x(
...
```

## Optimization:

Only consider terms  $d^M$  whose analog is unique up to theory-specific simplification ↓

SAT



# Enumerative Syntax-Guided Synthesis in SMT

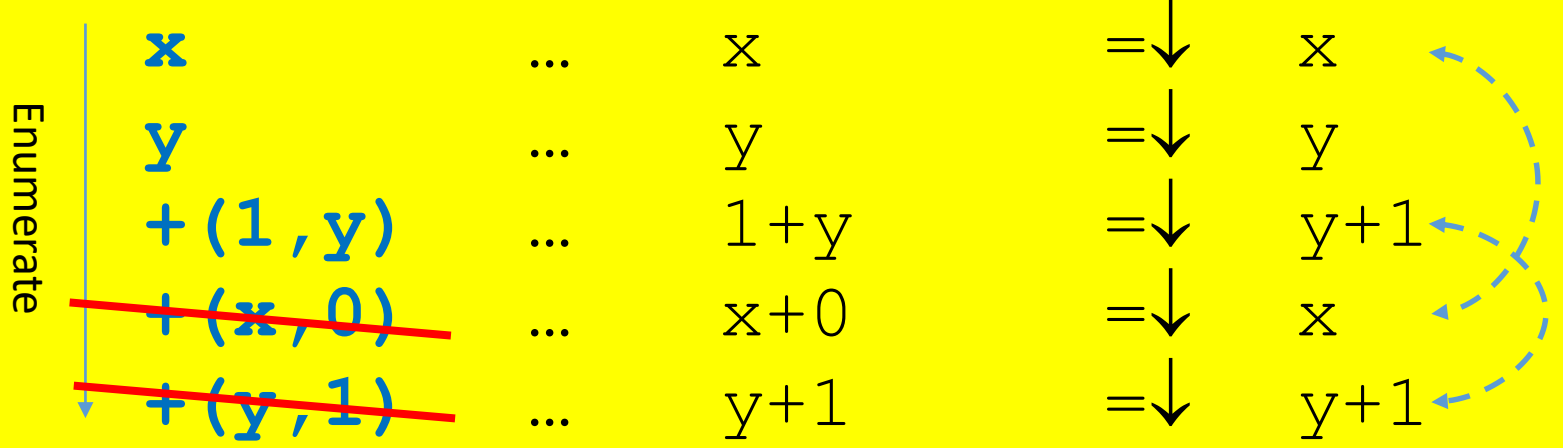
```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := >=(I, I) | =(I, I) | ¬(B)
```

```
is_x(d)vis(d)vis(d)vis(d)vis(d)vis(d)vis(d)
¬is_x(
...
```

## Optimization:

Only consider terms  $d^M$  whose analog is unique up to theory-specific simplification ↓

SAT



```
= ...
```

# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

...

Maintain a database  
of candidates whose simplified  
analogs are pairwise unique

I	Int	Int↓
<b>x</b>	x	x
<b>y</b>	y	y
<b>+(1, y)</b>	1+y	y+1

SAT Solver

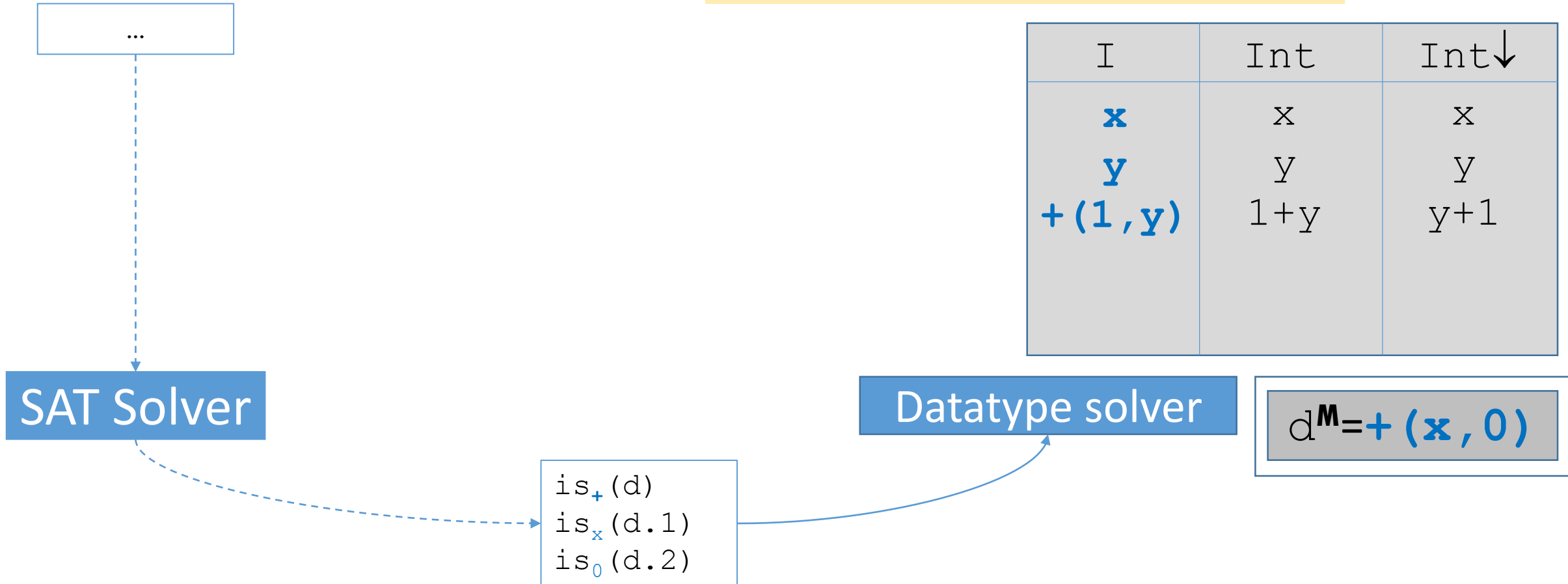
Datatype solver



# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

I	Int	Int↓
<b>x</b>	x	x
<b>y</b>	y	y
<b>+(1, y)</b>	1+y	y+1



# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

...

I	Int	Int↓
<b>x</b>	x	x
<b>y</b>	y	y
<b>+(1, y)</b>	1+y	y+1

SAT Solver

Datatype solver

$d^M = +(x, 0)$

$is_+(d)$   
 $is_x(d.1)$   
 $is_0(d.2)$

compute simplified analog

<b>+(x, 0)</b>	x+0	x
----------------	-----	---

# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

...

I	Int	Int↓
<b>x</b>	x	<b>x</b>
<b>y</b>	y	y
<b>+(1, y)</b>	1+y	y+1

SAT Solver

Datatype solver

$is_+(d)$   
 $is_x(d.1)$   
 $is_0(d.2)$

$d^M = +(x, 0)$

...not unique

<b>+(x, 0)</b>	x+0	<b>x</b>
----------------	-----	----------



# Enumerative Syntax-Guided Synthesis in SMT

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\neg \text{is}_+(d) \vee \neg \text{is}_x(d.1) \vee \neg \text{is}_0(d.2)$

- Return “symmetry breaking” clause  
For details, see [Reynolds et al FMSD2017]

SAT Solver

Datatype solver

$\text{is}_+(d)$   
 $\text{is}_x(d.1)$   
 $\text{is}_0(d.2)$

I	Int	Int↓
<b>x</b>	x	x
<b>y</b>	y	y
<b>+(1, y)</b>	1+y	y+1

$d^M = +(x, 0)$

<b>+(x, 0)</b>	x+0	x
----------------	-----	---

# SyGuS in SMT : Symmetry Breaking Clauses

$$\neg is_+(d) \vee \neg is_x(d.1) \vee \neg is_0(d.2)$$

“Do not consider solutions where  $d$  is  $+(x, 0)$ ”

# SyGuS in SMT : Symmetry Breaking Clauses

$$\neg is_+(d) \vee \neg is_x(d.1) \vee \neg is_0(d.2)$$

“Do not consider solutions where d is  $+(x, 0)$ ”

- Can be applied for any **subterm** of d:

$$\neg is_+(d.1) \vee \neg is_x(d.1.1) \vee \neg is_0(d.1.2)$$

“Do not consider solutions where the first child of d is  $+(x, 0)$ ”

# SyGuS in SMT : Symmetry Breaking Clauses

$$\neg is_+(d) \vee \neg is_x(d.1) \vee \neg is_0(d.2)$$

“Do not consider solutions where  $d$  is  $+(x, 0)$ ”

- Can be applied for any subterm of  $d$ :

$$\neg is_+(d.1) \vee \neg is_x(d.1.1) \vee \neg is_0(d.1.2)$$

“Do not consider solutions where the first child of  $d$  is  $+(x, 0)$ ”

- Can be **generalized**:

$$\neg is_+(d) \vee \neg is_0(d.2)$$

“Do not consider solutions where  $d$  is of the form  $+(t, 0)$  for any  $t$ ”

⇒ Leads to stronger search space pruning

# Enumerative SyGuS in SMT **for I/O Examples**

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := ≥(I, I) | =(I, I) | ¬(B)
```

## Optimization for I/O examples:

*Only consider terms  $d^M$  whose analog is unique up to evaluation on input examples*

I	Int	Int↓
		x
		y
		y+1

SAT Solver

Datatype solver



# Enumerative SyGuS in SMT for I/O Examples

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := ≥(I, I) | =(I, I) | ¬(B)
```

I	Int	Int↓
<b>x</b>	x	x
<b>y</b>	y	y
<b>+(1, y)</b>	1+y	y+1

SAT Solver

Datatype solver

# Enumerative SyGuS in SMT for I/O Examples

$I := x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B := \geq(I, I) \mid =(I, I) \mid \neg(B)$

I	Int	Int↓	Ex. Output
<b>x</b>	x	x	
<b>y</b>	y	y	
<b>+(1, y)</b>	1+y	y+1	

SAT Solver

Datatype solver

Maintain output  
for examples

# Enumerative SyGuS in SMT for I/O Examples

$I := x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B := \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\exists f. \forall xy. ((x=1 \wedge y=1) \Rightarrow f(x, y)=0) \wedge$   
 $((x=2 \wedge y=1) \Rightarrow f(x, y)=1) \wedge$   
 $((x=3 \wedge y=1) \Rightarrow f(x, y)=2)$

SAT Solver

I	Int	Int↓	Ex. Output
<b>x</b>	x	x	
<b>y</b>	y	y	
<b>+(1, y)</b>	1+y	y+1	

Datatype solver

# Enumerative SyGuS in SMT for I/O Examples

$I := x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B := \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\exists f. \forall xy. ((x=1 \wedge y=1) \Rightarrow f(x, y)=0) \wedge$   
 $((x=2 \wedge y=1) \Rightarrow f(x, y)=1) \wedge$   
 $((x=3 \wedge y=1) \Rightarrow f(x, y)=2)$

SAT Solver

I	Int	Int↓	Ex. Output
<b>x</b>	x	x	<b>1</b>
<b>y</b>	y	y	<b>1</b>
<b>+(1, y)</b>	1+y	y+1	<b>2</b>

Datatype solver

# Enumerative SyGuS in SMT for I/O Examples

$I := x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B := \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\exists f. \forall xy. ((x=1 \wedge y=1) \Rightarrow f(x, y)=0) \wedge$   
 $((x=2 \wedge y=1) \Rightarrow f(x, y)=1) \wedge$   
 $((x=3 \wedge y=1) \Rightarrow f(x, y)=2)$

SAT Solver

I	Int	Int↓	Ex. Output
<b>x</b>	x	x	1, <b>2</b>
<b>y</b>	y	y	1, <b>1</b>
<b>+(1, y)</b>	1+y	y+1	2, <b>2</b>

Datatype solver

# Enumerative SyGuS in SMT for I/O Examples

$I := x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B := \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\exists f. \forall xy. ((x=1 \wedge y=1) \Rightarrow f(x, y)=0) \wedge$   
 $((x=2 \wedge y=1) \Rightarrow f(x, y)=1) \wedge$   
 $((x=3 \wedge y=1) \Rightarrow f(x, y)=2)$

SAT Solver

I	Int	Int↓	Ex. Output
<b>x</b>	x	x	1, 2, <b>3</b>
<b>y</b>	y	y	1, 1, <b>1</b>
<b>+(1, y)</b>	1+y	y+1	2, 2, <b>2</b>

Datatype solver

# Enumerative SyGuS in SMT for I/O Examples

$I := x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B := \geq(I, I) \mid =(I, I) \mid \neg(B)$

I	Int	Int↓	Ex. Output
<b>x</b>	x	x	1, 2, 3
<b>y</b>	y	y	1, 1, 1
<b>+(1, y)</b>	1+y	y+1	2, 2, 2

SAT Solver

Datatype solver

# Enumerative SyGuS in SMT for I/O Examples

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

I	Int	Int $\downarrow$	Ex. Output
$x$	$x$	$x$	$1, 2, 3$
$y$	$y$	$y$	$1, 1, 1$
$+(1, y)$	$1+y$	$y+1$	$2, 2, 2$

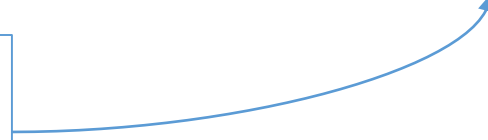
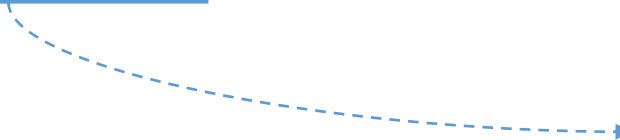
SAT Solver

Datatype solver

$d^M = +(1, 1)$

$is_+(d)$   
 $is_1(d.1)$   
 $is_1(d.2)$

...





# Enumerative SyGuS in SMT for I/O Examples

...

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

I	Int	Int↓	Ex. Output
$x$	$x$	$x$	$1, 2, 3$
$y$	$y$	$y$	$1, 1, 1$
$+(1, y)$	$1+y$	$y+1$	$2, 2, 2$

SAT Solver

Datatype solver

$d^M = +(1, 1)$

$is_+(d)$   
 $is_1(d.1)$   
 $is_1(d.2)$

compute simplified analog

$+(1, 1)$

$1+1$

$2$

$+(1, 1)$	$1+1$	$2$	
-----------	-------	-----	--

# Enumerative SyGuS in SMT for I/O Examples

```
I := x | y | 0 | 1 | +(I, I) | ite(B, I, I)
B := >=(I, I) | =(I, I) | ¬(B)
```

...

```
∃f. ∀xy. ((x=1 ∧ y=1) ⇒ f(x, y)=0) ∧
          ((x=2 ∧ y=1) ⇒ f(x, y)=1) ∧
          ((x=3 ∧ y=1) ⇒ f(x, y)=2)
```

I	Int	Int↓	Ex. Output
<b>x</b>	x	x	1, 2, 3
<b>y</b>	y	y	1, 1, 1
<b>+(1, y)</b>	1+y	y+1	2, 2, 2

SAT Solver

Datatype solver

$d^M = +(1, 1)$

```
is+(d)
is1(d.1)
is1(d.2)
```

...and example outputs

<b>+(1, 1)</b>	1+1	<b>2</b>	<b>2, 2, 2</b>
----------------	-----	----------	----------------

# Enumerative SyGuS in SMT for I/O Examples

...

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

I	Int	Int↓	Ex. Output
<b>x</b>	x	x	1, 2, 3
<b>y</b>	y	y	1, 1, 1
<b>+(1, y)</b>	1+y	y+1	2, 2, 2

SAT Solver

Datatype solver

$d^M = +(1, 1)$

$is_+(d)$   
 $is_1(d.1)$   
 $is_1(d.2)$

If not unique...

<b>+(1, 1)</b>	1+1	2	2, 2, 2
----------------	-----	---	---------



# Enumerative SyGuS in SMT for I/O Examples

$I ::= x \mid y \mid 0 \mid 1 \mid +(I, I) \mid \text{ite}(B, I, I)$   
 $B ::= \geq(I, I) \mid =(I, I) \mid \neg(B)$

$\neg \text{is}_+(d) \vee \neg \text{is}_1(d.1) \vee \dots \vee \neg \text{is}_1(d.2)$

- Return symmetry breaking clause  
 $\Rightarrow$  These also can be generalized and are applicable to any subterm of  $d$

I	Int	Int↓	Ex. Output
$x$	$x$	$x$	1, 2, 3
$y$	$y$	$y$	1, 1, 1
$+(1, y)$	$1+y$	$y+1$	2, 2, 2

SAT Solver

Datatype solver

$\text{is}_+(d)$   
 $\text{is}_1(d.1)$   
 $\text{is}_1(d.2)$

$d^M = +(1, 1)$

$+(1, 1)$	$1+1$	2	2, 2, 2
-----------	-------	---	---------

# Overview

With Syntactic Restrictions	<p>Enumerative SyGuS</p> <p>+ I/O Symmetry Breaking</p>	<p>Enumerative SyGuS</p> <p>CEGQI + reconstruction</p>	<p>Enumerative SyGuS</p>
Without Syntactic Restrictions	<p>CEGQI (trivially)</p>	<p>Counterexample Guided <math>\forall</math>-Instantiation</p>	<p>Enumerative SyGuS (using default restrictions)</p>
Input/Output Examples	Single Invocation Conjectures		Other Second-Order Synthesis Conjectures

What if conjecture is *Partially Single Invocation*?

$$\exists I. \forall x x'. (\text{pre}(x) \Rightarrow I(x)) \wedge ((I(x) \wedge T(x, x')) \Rightarrow I(x')) \wedge (I(x) \Rightarrow \text{post}(x))$$

E.g. invariant synthesis problem for  $I$  w.r.t  $\text{pre}$ ,  $T$ ,  $\text{post}$

# What if conjecture is *Partially Single Invocation*?

$$\exists I. \forall x x'. (\text{pre}(x) \Rightarrow I(x)) \wedge ((I(x) \wedge T(x, x')) \Rightarrow I(x')) \wedge (I(x) \Rightarrow \text{post}(x))$$

Partition into...

$$\exists I. \forall x. (\text{pre}(x) \Rightarrow \mathbf{I(x)} ) \wedge (\mathbf{I(x)} \Rightarrow \text{post}(x))$$

Single-invocation portion

$$\exists I. \forall x x'. (\mathbf{I(x)} \wedge T(x, x')) \Rightarrow \mathbf{I(x')}$$

Non-single-invocation portion

# What if conjecture is *Partially Single Invocation*?

$\exists I. \forall x x'. (\text{pre}(x) \Rightarrow I(x)) \wedge ((I(x) \wedge T(x, x')) \Rightarrow I(x')) \wedge (I(x) \Rightarrow \text{post}(x))$

$\exists I. \forall x. (\text{pre}(x) \Rightarrow I(x)) \wedge (I(x) \Rightarrow \text{post}(x))$

$\exists I. \forall x x'. (I(x) \wedge T(x, x')) \Rightarrow I(x')$

SMT Solver

Counterexample  
Guided  
 $\forall$ -Instantiation

unsat

$\lambda x. \text{ite}((\text{pre}(x) \Rightarrow \mathbf{T}) \wedge (\mathbf{T} \Rightarrow \text{post}(x)), \mathbf{T}, \perp)$



# What if conjecture is *Partially Single Invocation*?

$$\exists I. \forall x x'. (\text{pre}(x) \Rightarrow I(x)) \wedge ((I(x) \wedge T(x, x')) \Rightarrow I(x')) \wedge (I(x) \Rightarrow \text{post}(x))$$

$$\exists I. \forall x. (\text{pre}(x) \Rightarrow I(x)) \wedge (I(x) \Rightarrow \text{post}(x))$$

$$\exists I. \forall x x'. (I(x) \wedge T(x, x')) \Rightarrow I(x')$$

SMT Solver

Counterexample  
Guided  
 $\forall$ -Instantiation

unsat

$\lambda x. \text{post}(x)$

# What if conjecture is *Partially Single Invocation*?

$$\exists I. \forall x x'. (\text{pre}(x) \Rightarrow I(x)) \wedge ((I(x) \wedge T(x, x')) \Rightarrow I(x')) \wedge (I(x) \Rightarrow \text{post}(x))$$

$$\exists I. \forall x. (\text{pre}(x) \Rightarrow I(x)) \wedge (I(x) \Rightarrow \text{post}(x))$$

$$\exists I. \forall x x'. (I(x) \wedge T(x, x')) \Rightarrow I(x')$$

SMT Solver

Counterexample  
Guided  
 $\forall$ -Instantiation

unsat

$\lambda x. \text{post}(x)$

Candidate invariant  
 $\Rightarrow$  check against non-single  
invocation portion

# What if conjecture is *Partially Single Invocation*?

$$\exists I. \forall x x'. (\text{pre}(x) \Rightarrow I(x)) \wedge ((I(x) \wedge T(x, x')) \Rightarrow I(x')) \wedge (I(x) \Rightarrow \text{post}(x))$$

$$\exists I. \forall x. (\text{pre}(x) \Rightarrow I(x)) \wedge (I(x) \Rightarrow \text{post}(x)) \wedge S'$$

$$\exists I. \forall x x'. (I(x) \wedge T(x, x')) \Rightarrow I(x')$$

$\lambda x. \text{post}(x)$  solution?

No,  
infer new single  
invocation constraints  
 $S'$

Yes  
 $\lambda x. \text{post}(x)$

**SMT Solver**

Counterexample  
Guided  
 $\forall$ -Instantiation

# What if conjecture is *Partially Single Invocation*?

$$\exists I. \forall x x'. (\text{pre}(x) \Rightarrow I(x)) \wedge ((I(x) \wedge T(x, x')) \Rightarrow I(x')) \wedge (I(x) \Rightarrow \text{post}(x))$$

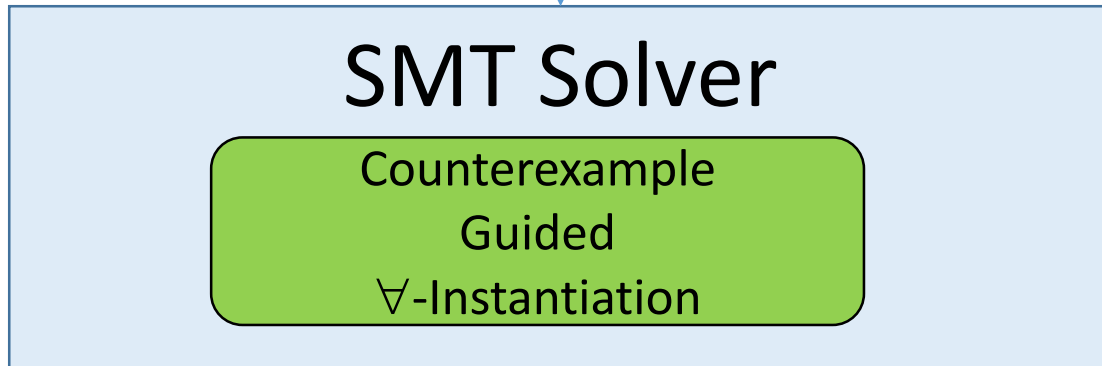
$$\exists I. \forall x. (\text{pre}(x) \Rightarrow I(x)) \wedge (I(x) \Rightarrow \text{post}(x)) \wedge S'$$

$$\exists I. \forall x x'. (I(x) \wedge T(x, x')) \Rightarrow I(x')$$

$\lambda x. \text{post}(x)$  solution?

No,  
infer new single  
invocation constraints  
 $S'$

Yes  
 $\lambda x. \text{post}(x)$

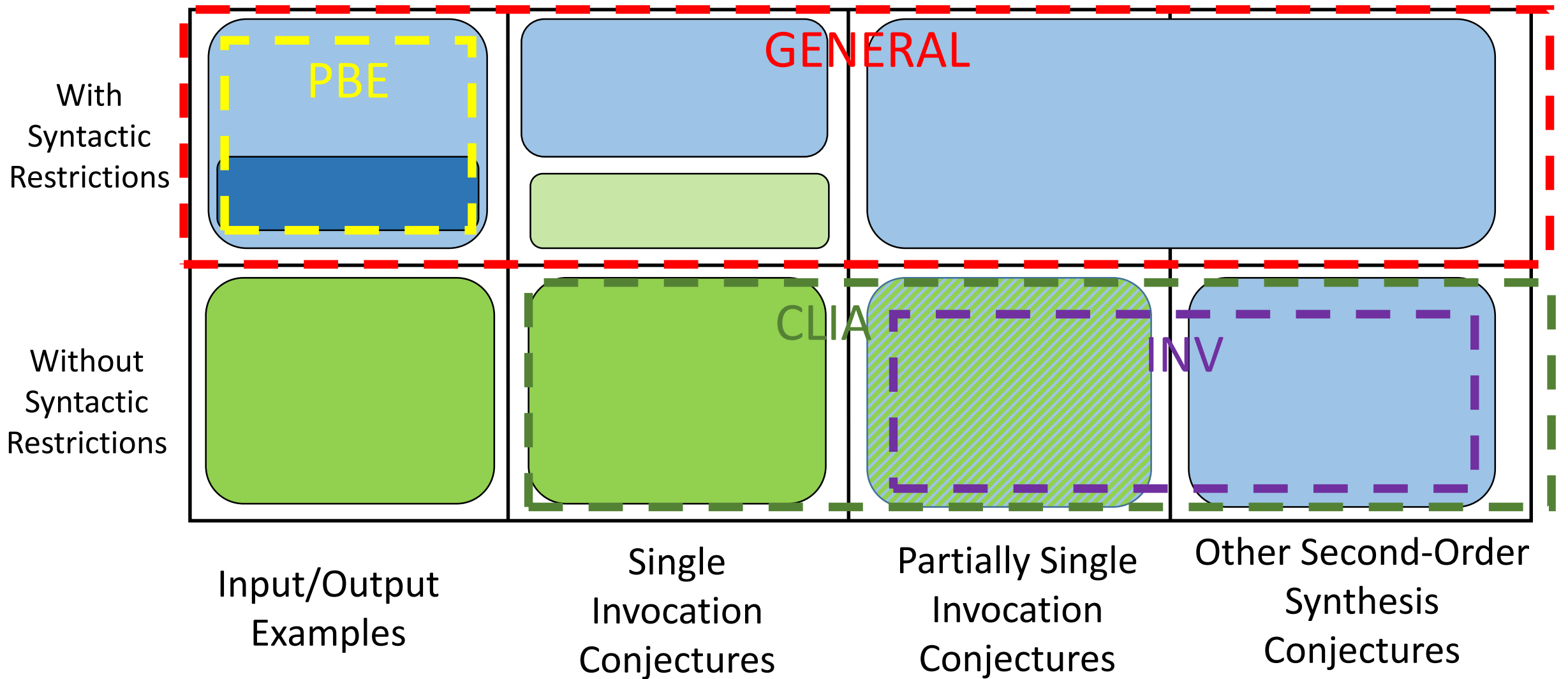


$\Rightarrow$  Related to property-directed reachability (PDR) **Bradley 2011**

# Overview

With Syntactic Restrictions	<p>Enumerative SyGuS</p> <p>+ I/O Symmetry Breaking</p>	<p>Enumerative SyGuS</p> <p>CEGQI + reconstruction</p>	<p>Enumerative SyGuS</p>	
Without Syntactic Restrictions	<p>CEGQI (trivially)</p>	<p>Counterexample Guided <math>\forall</math>-Instantiation</p>	<p>Hybrid approaches?</p>	<p>Enumerative SyGuS (using default restrictions)</p>
	Input/Output Examples	Single Invocation Conjectures	Partially Single Invocation Conjectures	Other Second-Order Synthesis Conjectures

# CVC4 for SyGuS Comp 2017:



# Topics Not Covered:

- Automatically inferring when a conjecture  $\Leftrightarrow$  a single-invocation one
- CEGQI solution minimization by proof analysis
  - Simpler proofs  $\Rightarrow$  shorter functions
- Approaches inspired by synthesis by unification [\[Alur et al TACAS2017\]](#)
  - Decision tree learning for `ite`-solutions for PBE Bit-Vectors
  - Sequencing algorithm for `concat`-solutions for PBE Strings
- Cases when CEGQI can benefit from syntax-guided synthesis
  - SMT approaches for  $\forall$ +BV may benefit from SyGuS [\[Preiner et al TACAS2017\]](#)

- ...Thanks for listening!
  
- SyGuS solver in master branch of CVC4:
  - Open source
  - Available at : <http://cvc4.cs.stanford.edu/web/>
  - Accepts \*.smt2, \*.sy formats
  - ...many other features

