

# FROM LTL TO rLTL MONITORING

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Joint Work With Corto Mascle, Daniel Neider, Paulo Tabuada,  
Alexander Weinert, Martin Zimmermann

# WHY rLTL RATHER THAN LTL?

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Assumption  $\implies$  Guarantee

View Always Unobstructed  $\implies$  Always Stay on Lane

$G(\text{unobs. view}) \implies G(\text{on lane})$

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# WHY rLTL RATHER THAN LTL?

**LTL to rLTL:  
More Robustness  
More Information**

View Always

Assumption  $\implies$  Guarantee  
Unobstructed  $\implies$  Always Stay  
 $G(\text{unobs.view}) \implies G(\text{on lane})$

on Lane

Problem 1: One Frame Camera Glitch  $\implies$  Do Whatever You Want

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# Lift Monitoring from LTL to rLTL

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**rLTL on  
Finite Traces**



**Construction  
of an rLTL  
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**Case Study:  
LTL v. rLTL**

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# What you need to know about (r)LTL semantics

LTL

$a \in \Sigma, \quad AP = 2^\Sigma, \quad \text{trace } \pi \in AP^\omega$

Example

$\varphi \equiv a$

$\pi = \{a\} * * *$

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$$\pi = \{a\} \{a\} \{a\} \{a\}$$

$$\varphi \equiv \mathbf{F} a$$

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# Finite Semantics: Ternary Output

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1 – Already Satisfied

0 – Already Falsified

? – Don't Know

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Formula	Prefix	LTL	rLTL ( <b>G, FG, GF, F</b> )
<b>Ga</b>	$\varepsilon$	?	????
	{a}	?	???1
	{a}{ }	0	0???1

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Questions: What truth values might occur?

# Finite Semantics: Realizable Verdicts

<i>Value</i>	<i>Prefix</i>	<i>Formula</i>	<i>Value</i>	<i>Prefix</i>	<i>Formula</i>
0000	$\varepsilon$	$a \wedge \neg a$	0?11	$\emptyset\{a\}$	$\Box a \vee \Box \neg a$
000?	$\varepsilon$	$\Diamond \Box a \wedge \Diamond \neg \Diamond a$	0111	$\emptyset\{a\}$	$a \mathbf{R} a$
0001	unrealizable		????	$\varepsilon$	$\Box a$
00??	$\varepsilon$	$\Box a \wedge \Box \neg a$	????1	$\{a\}$	$\Box a$
00?1	$\emptyset\{a\}$	$\Box a \wedge \Box \neg a$	??11	$\varepsilon$	$\Box a \vee \Diamond \neg \Diamond a$
0011	unrealizable		?111	$\varepsilon$	$\Box a \vee \neg \Diamond \neg \Diamond \neg a$
0???	$\emptyset$	$\Box a$	1111	$\varepsilon$	$a \vee \neg a$
0??1	$\emptyset\{a\}$	$\Box a$			

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00??	$\varepsilon$	$\Box a \wedge \Box \neg a$	???1	$\{a\}$	$\Box a$
00?1	$\emptyset\{a\}$	$\Box a \wedge \Box \neg a$	??11	$\varepsilon$	$\Box a \vee \Diamond \neg \Diamond a$
0011	unrealizable		?111	$\varepsilon$	$\Box a \vee \neg \Diamond \neg \Diamond \neg a$
0???	$\emptyset$	$\Box a$	1111	$\varepsilon$	$a \vee \neg a$
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**Theorem: An rLTL Monitor cannot yield 0001 nor 0011.**

# Finite Semantics: Ternary Output

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Questions: How do values "evolve"?

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Questions: How do values "evolve"?

Theorem: Up to four refinements are possible.

# Monitorability

**rLTL-Ugly Prefix:** Every continuation yields **????**

**rLTL-Monitorable:** There are no **rLTL-Ugly Prefixes**

	LTL Monitorable	Not LTL Monitorable
rLTL Monitorable	<b>Ga</b>	<b>GFa</b>
Not rLTL Monitorable	<b>(Ga ∧ G¬a) ⇒ (FGa ∧ F¬a)</b>	<b>(p ∧ φ<sub>LTL</sub>) ∨ (¬p ∧ φ<sub>rLTL</sub>)</b>

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rLTL Monitorable	rLTL: "Adding { } will always yield 0****" $\mathbf{Ga}$ LTL: "Adding { } will always yield 0"	$\mathbf{GFa}$
Not rLTL Monitorable	$\mathbf{(Ga \wedge G\neg a) \implies (FGa \wedge FG\neg a)}$	$\mathbf{(p \wedge \varphi_{LTL}) \vee (\neg p \wedge \varphi_{rLTL})}$

# Monitorability

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rLTL Monitorable	rLTL: "Adding { } will always yield 0***" $\mathbf{Ga}$ LTL: "Adding { } will always yield 0"	rLTL: "Adding {a} will yield 1 in last bit" $\mathbf{GFa}$ LTL: "Depends on infinite behavior."
Not rLTL Monitorable	$(\mathbf{Ga} \wedge \mathbf{G}\neg a) \implies (\mathbf{FGa} \wedge \mathbf{FG}\neg a)$	$(p \wedge \varphi_{\text{LTL}}) \vee (\neg p \wedge \varphi_{\text{rLTL}})$

# LTL-MON DOES NOT IMPLY rLTL-MON

rLTL-Ugly Prefix: Every continuation yields ????

rLTL-Monitorable: There are no rLTL-Ugly Prefixes

$$(G a \wedge G \neg a) \implies (F G a \wedge F \underline{G} \neg a)$$

LTL-mon

$(G a \wedge G \neg a)$ : Contradiction

$(G a \wedge G \neg a) \implies (F G a \wedge F \underline{G} \neg a)$ : Tautology

Ugly Prefix { } $\{a\}$

$\forall \rho$ : { } $\{a\}\rho\{ \}^\omega$  yields 1111

{ } $\{a\}\rho\{a\}^\omega$  yields 0000

Not rLTL-mon

# Monitorability

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# Lift Monitoring from LTL to rLTL

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**rLTL on  
Finite Traces**

```
graph LR; A[rLTL on Finite Traces] --> B[Construction of an rLTL Monitor]; B --> C[Case Study: LTL v. rLTL]
```

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# Constructing an rLTL Monitor

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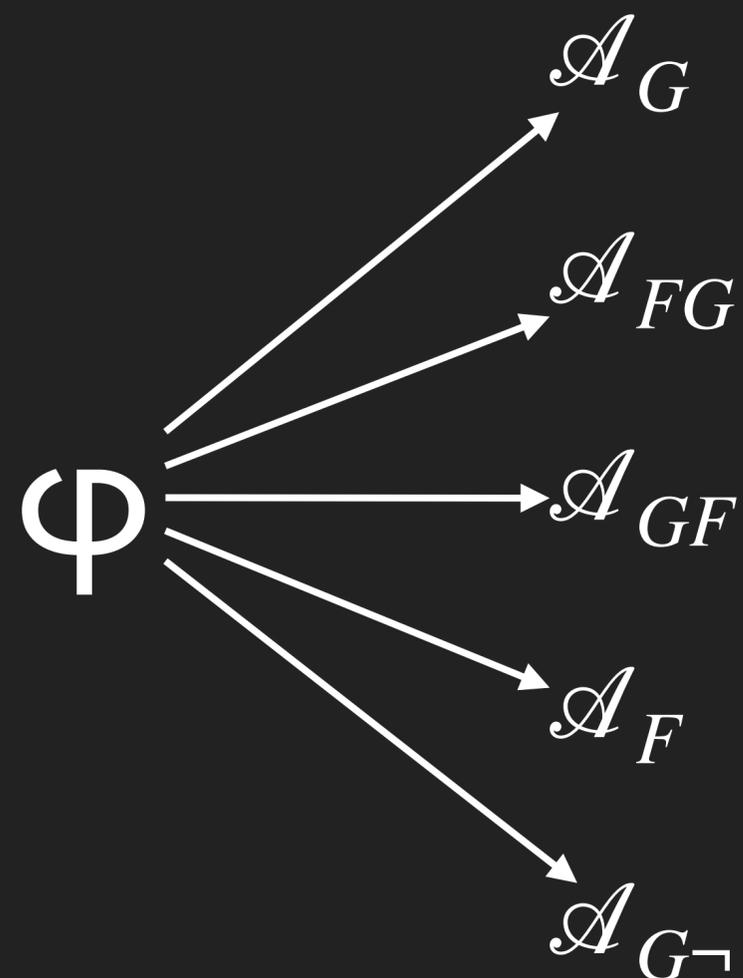
rLTL

$\Phi$

# Constructing an rLTL Monitor

rLTL

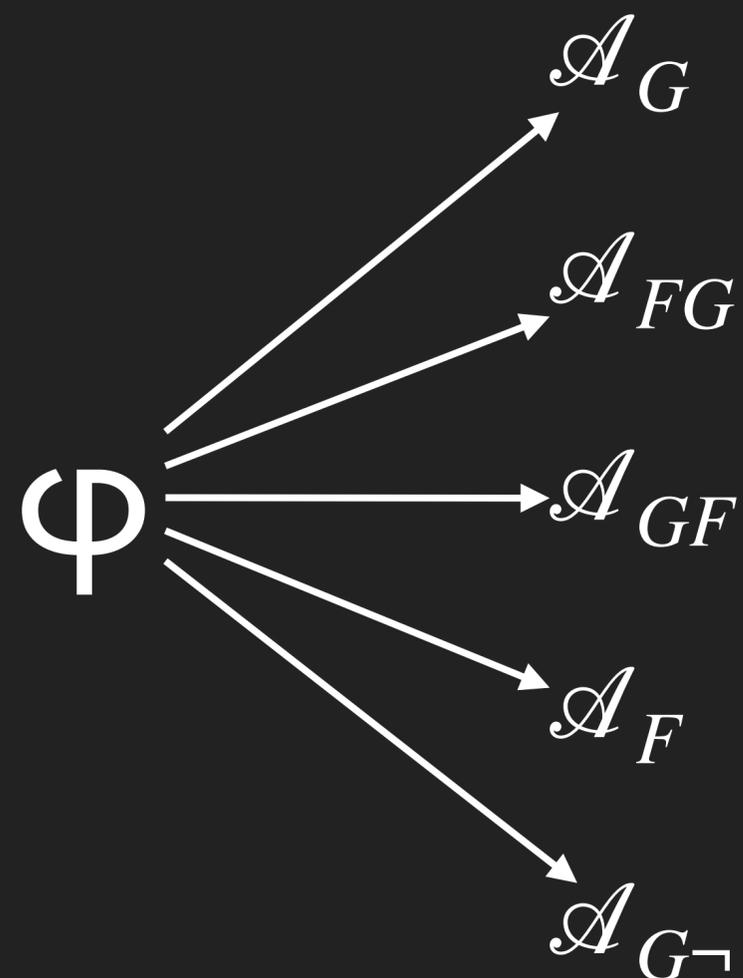
Büchi



# Constructing an rLTL Monitor

rLTL

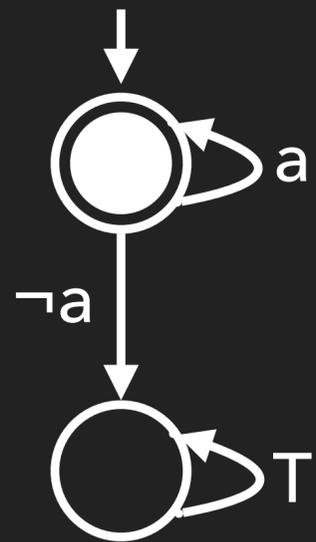
Büchi



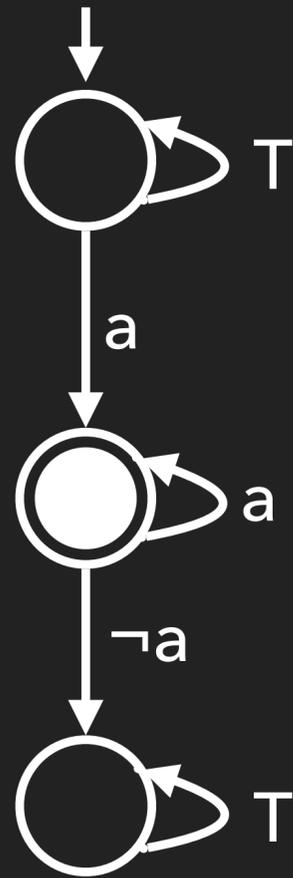
$$2^{O(|\varphi|)}$$

# Constructing an rLTL Monitor

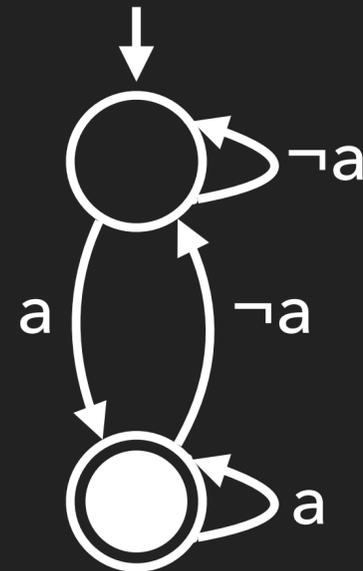
$G a$



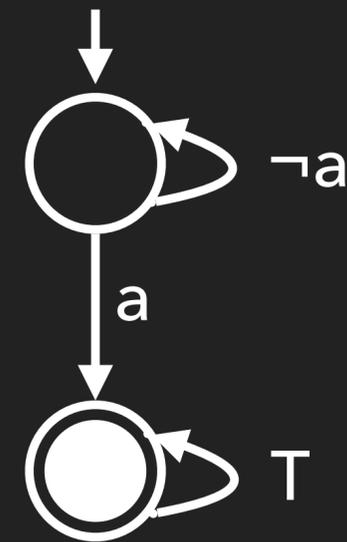
$F G a$



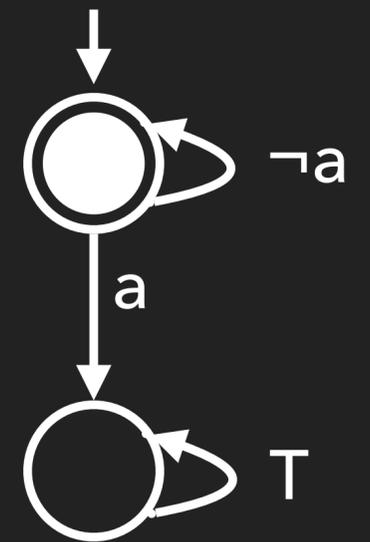
$G F a$



$F a$



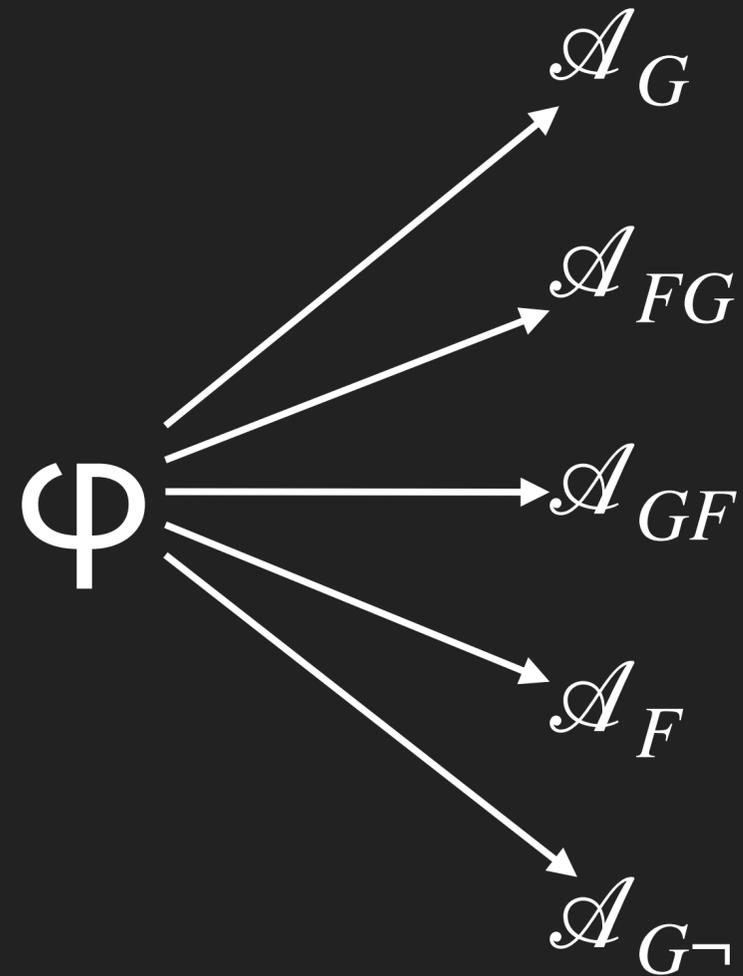
$G \neg a$



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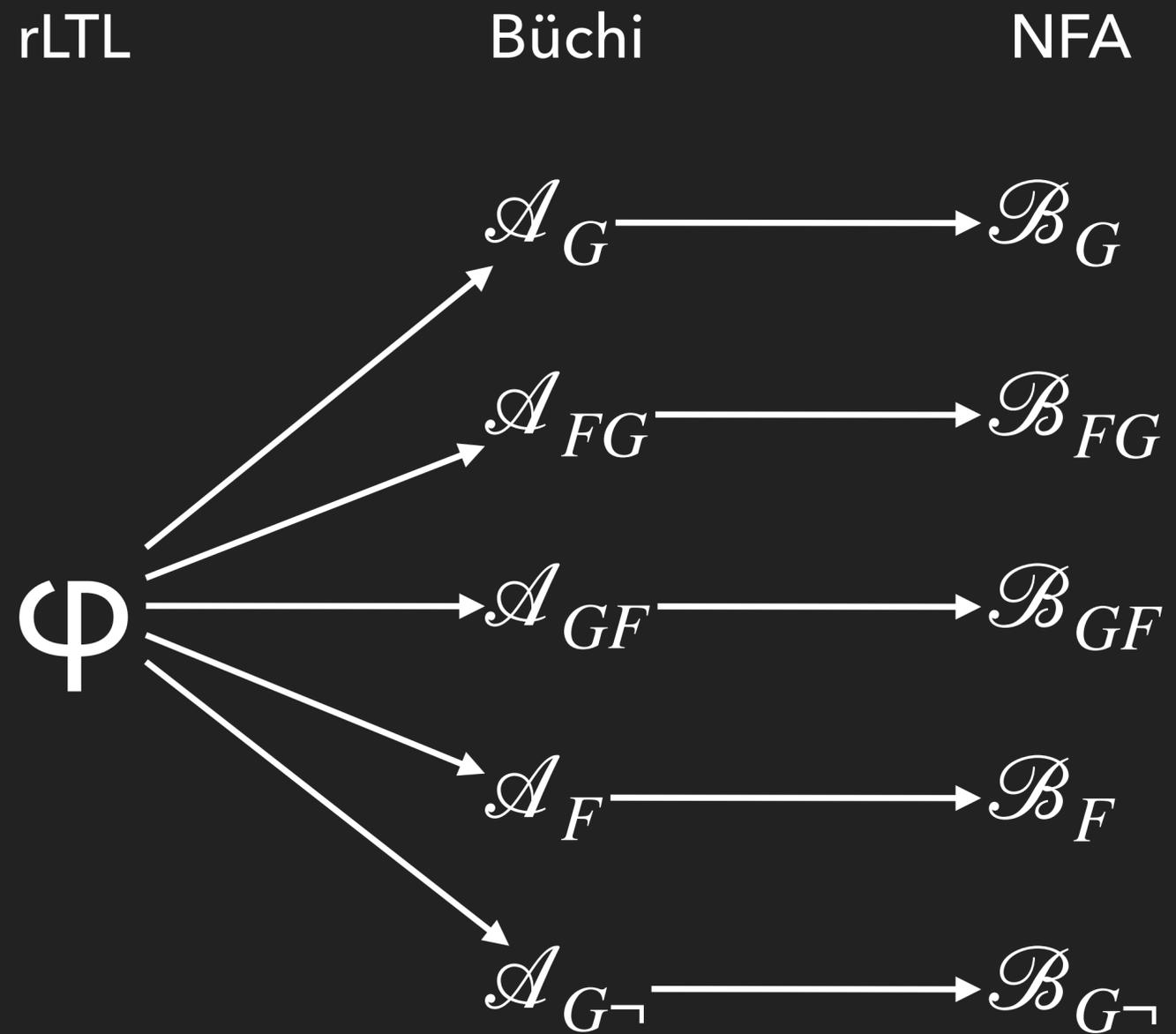
rLTL

Büchi



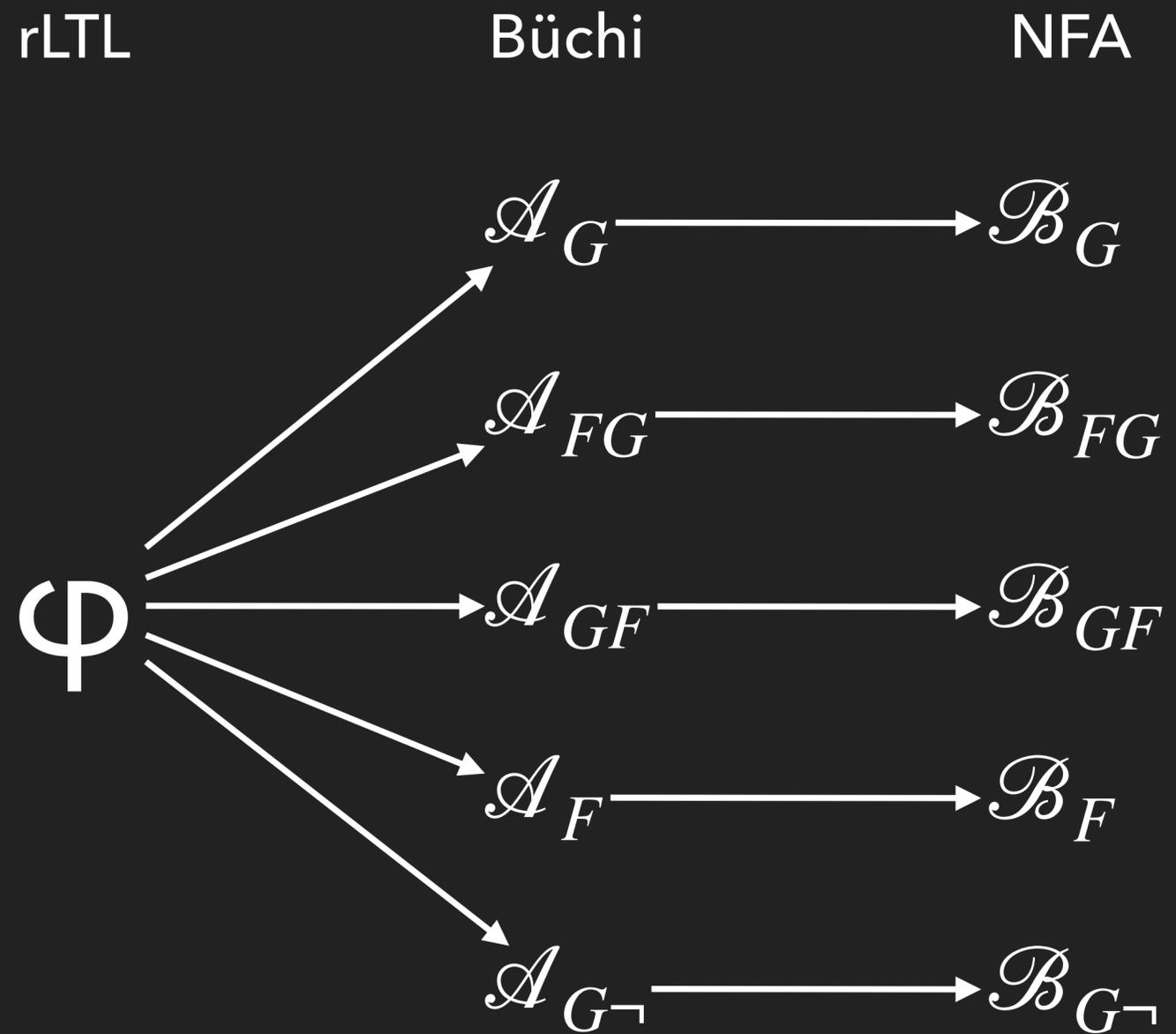
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# Constructing an rLTL Monitor



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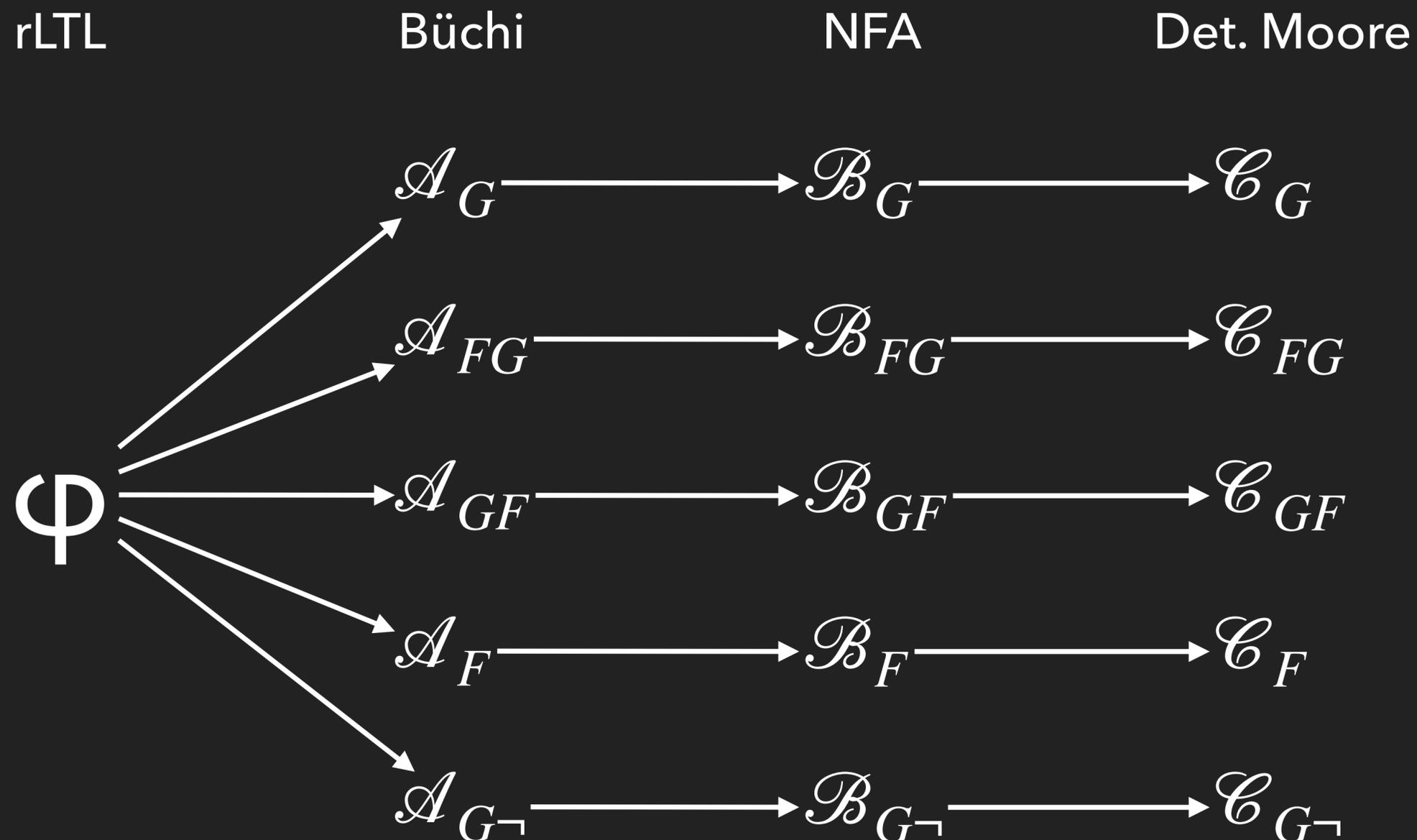
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$$2^{O(|\varphi|)}$$

$$O(|\mathcal{A}|^3)$$

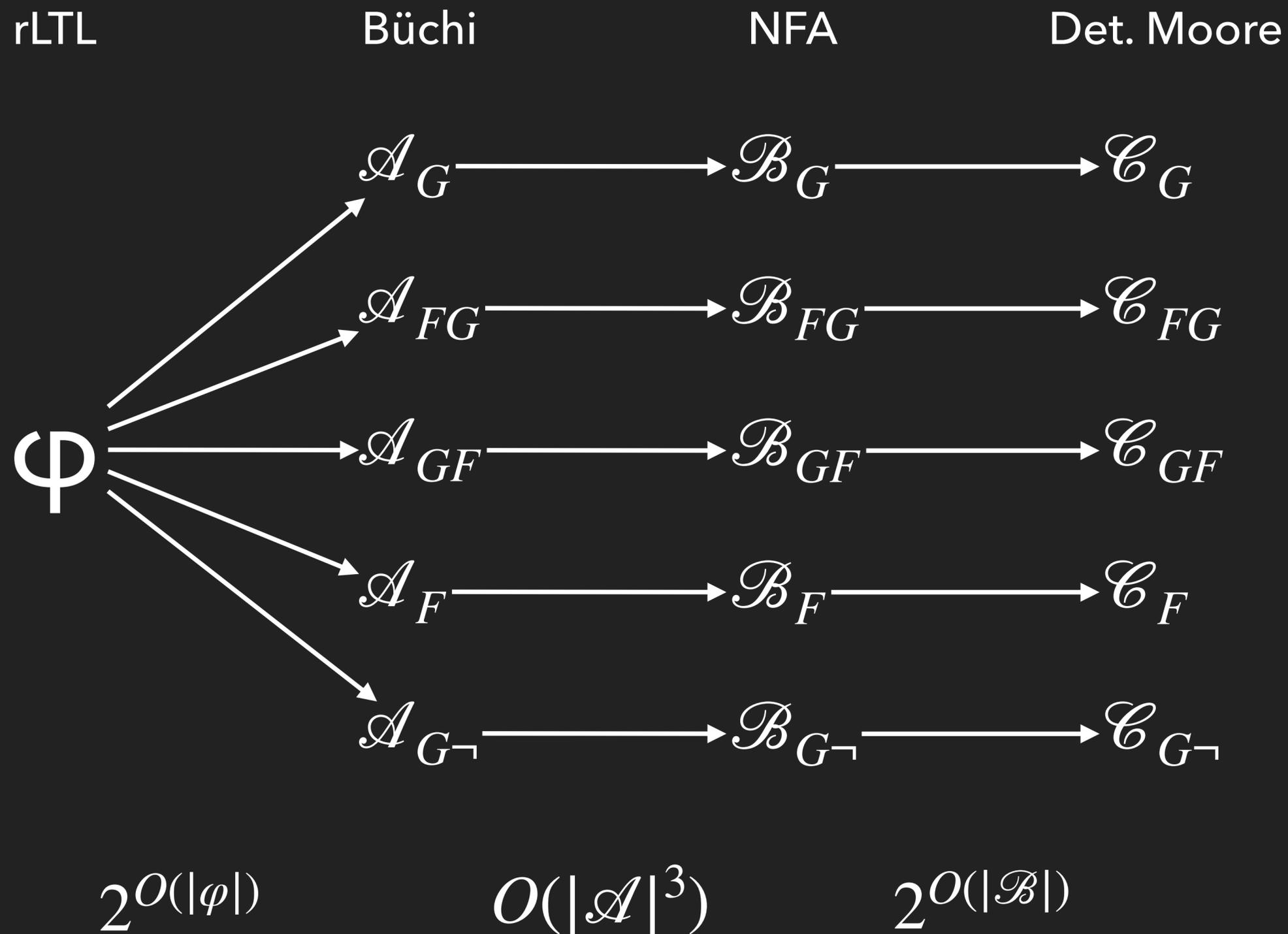
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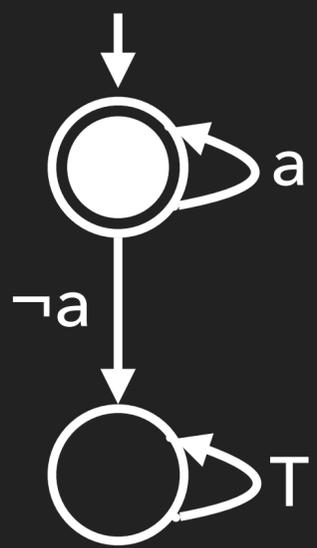
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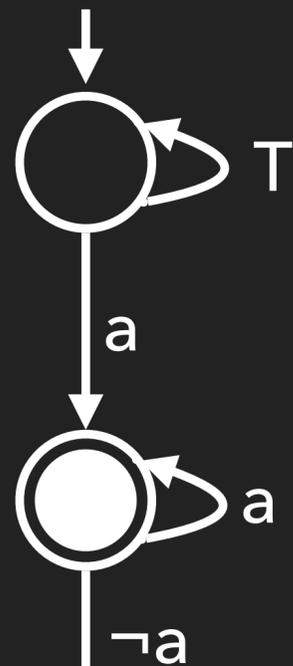


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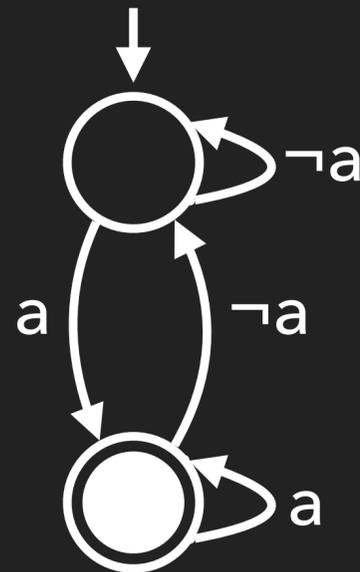
$G a$



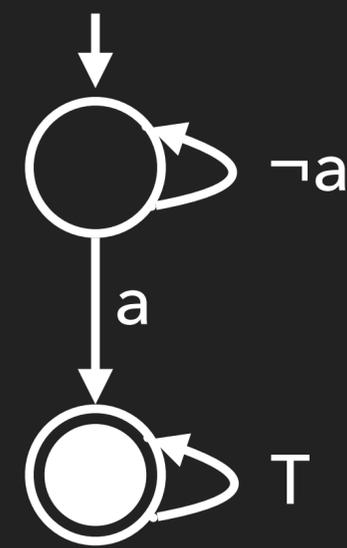
$FG a$



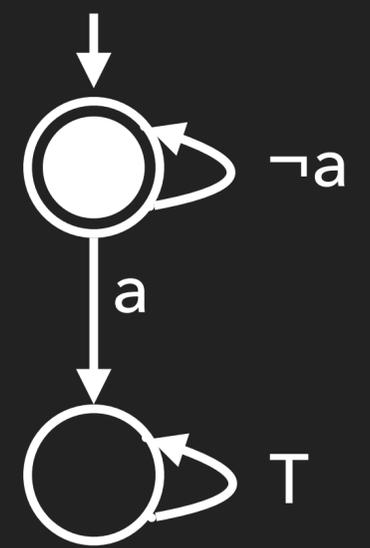
$GF a$



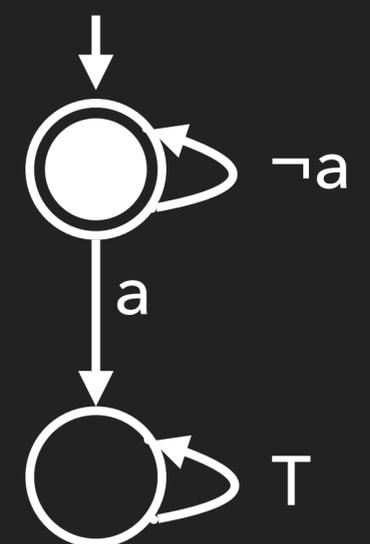
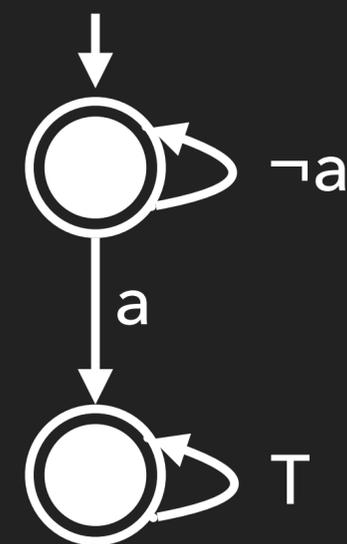
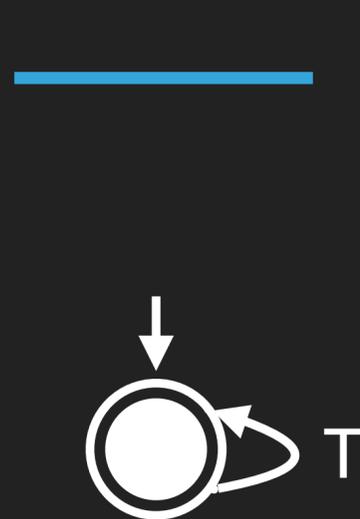
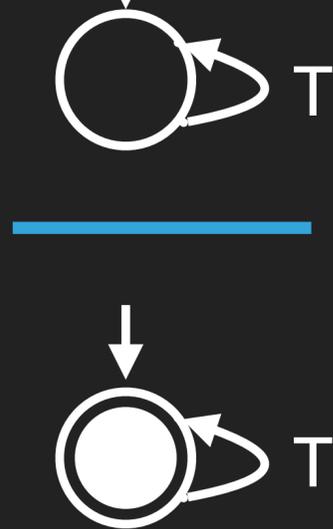
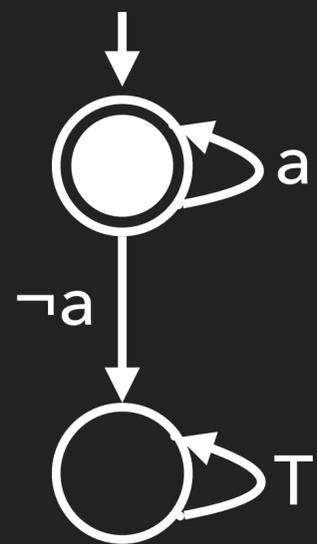
$F a$



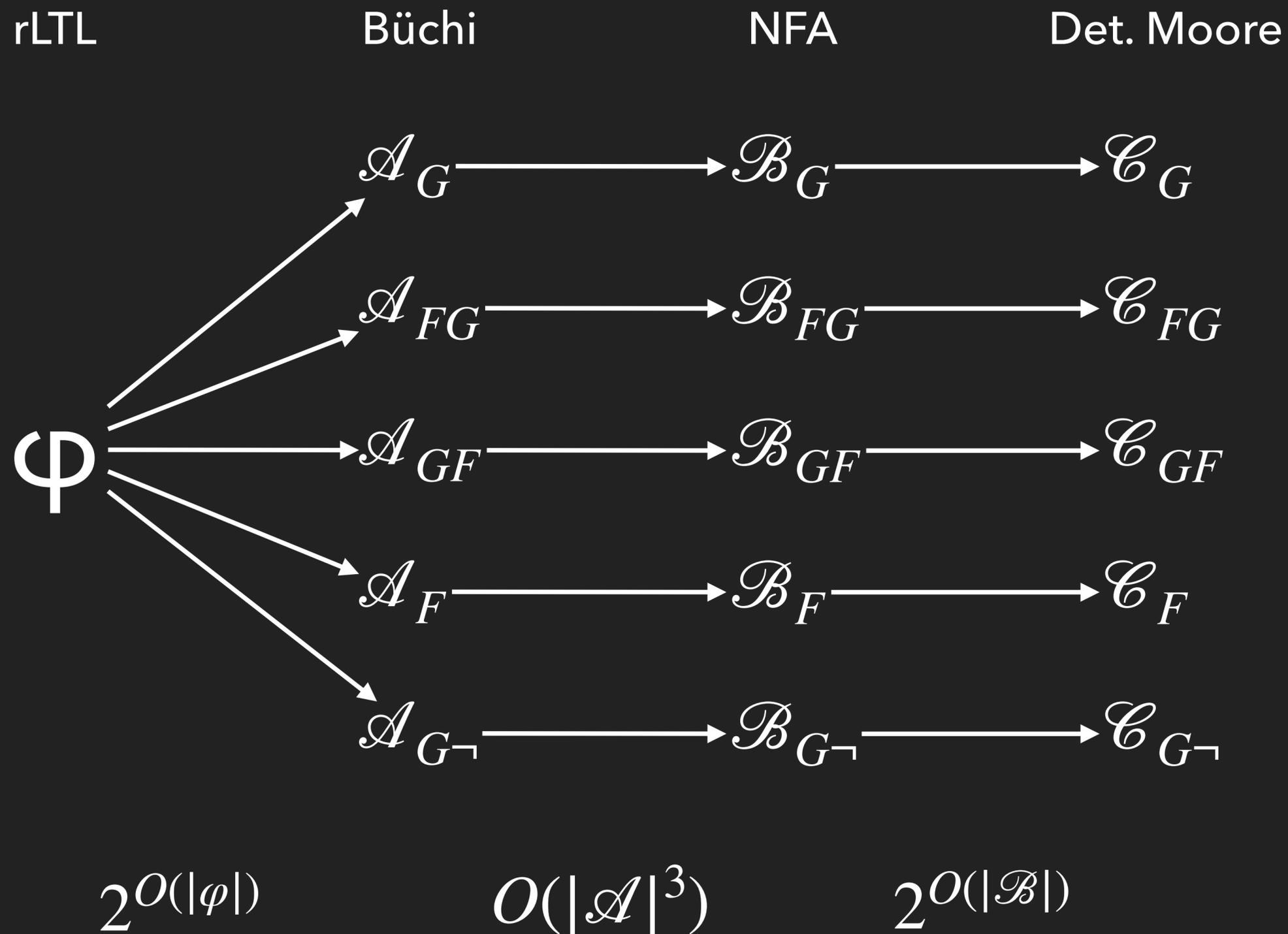
$G \neg a$



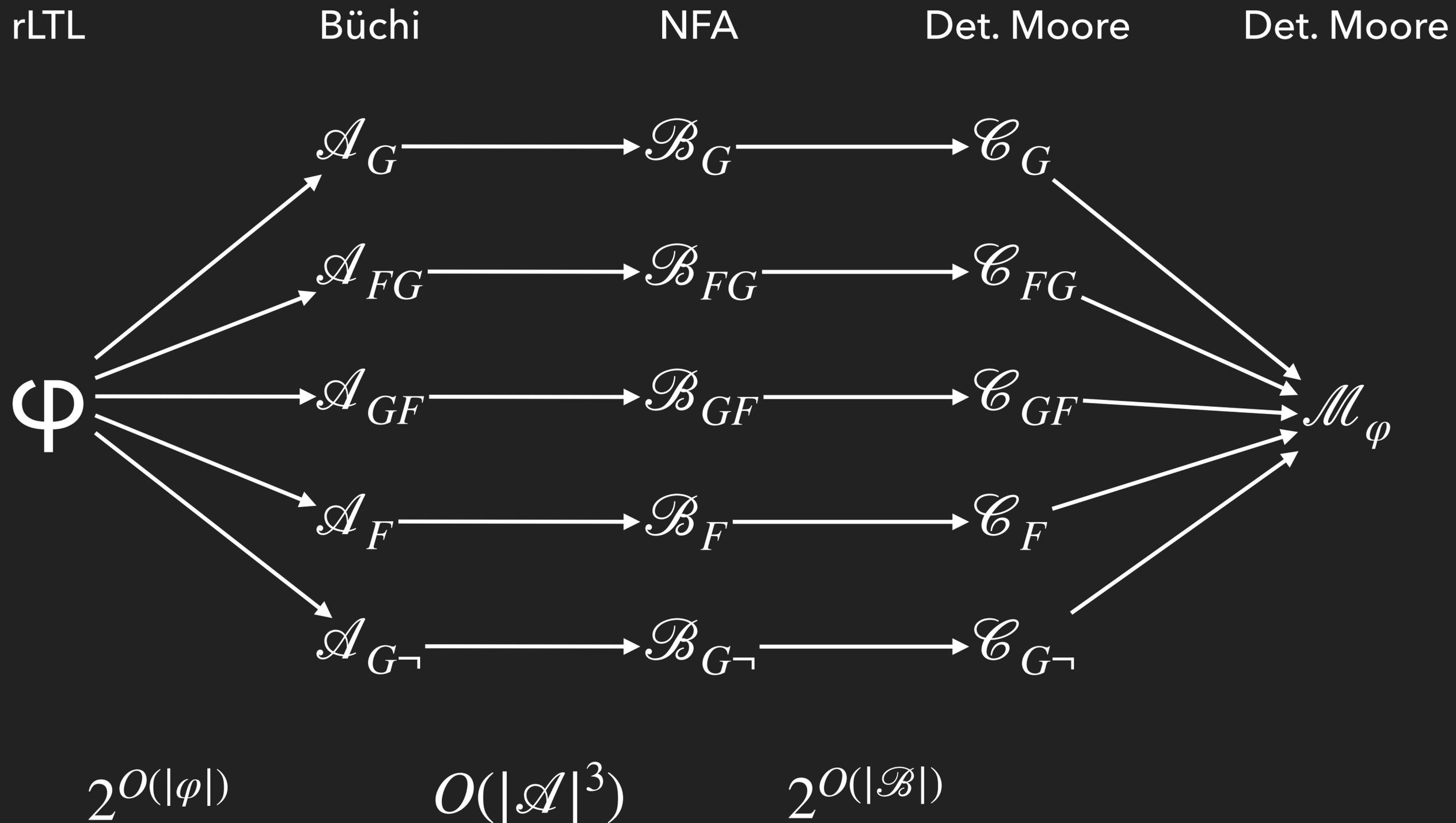
Büchi  
DFA



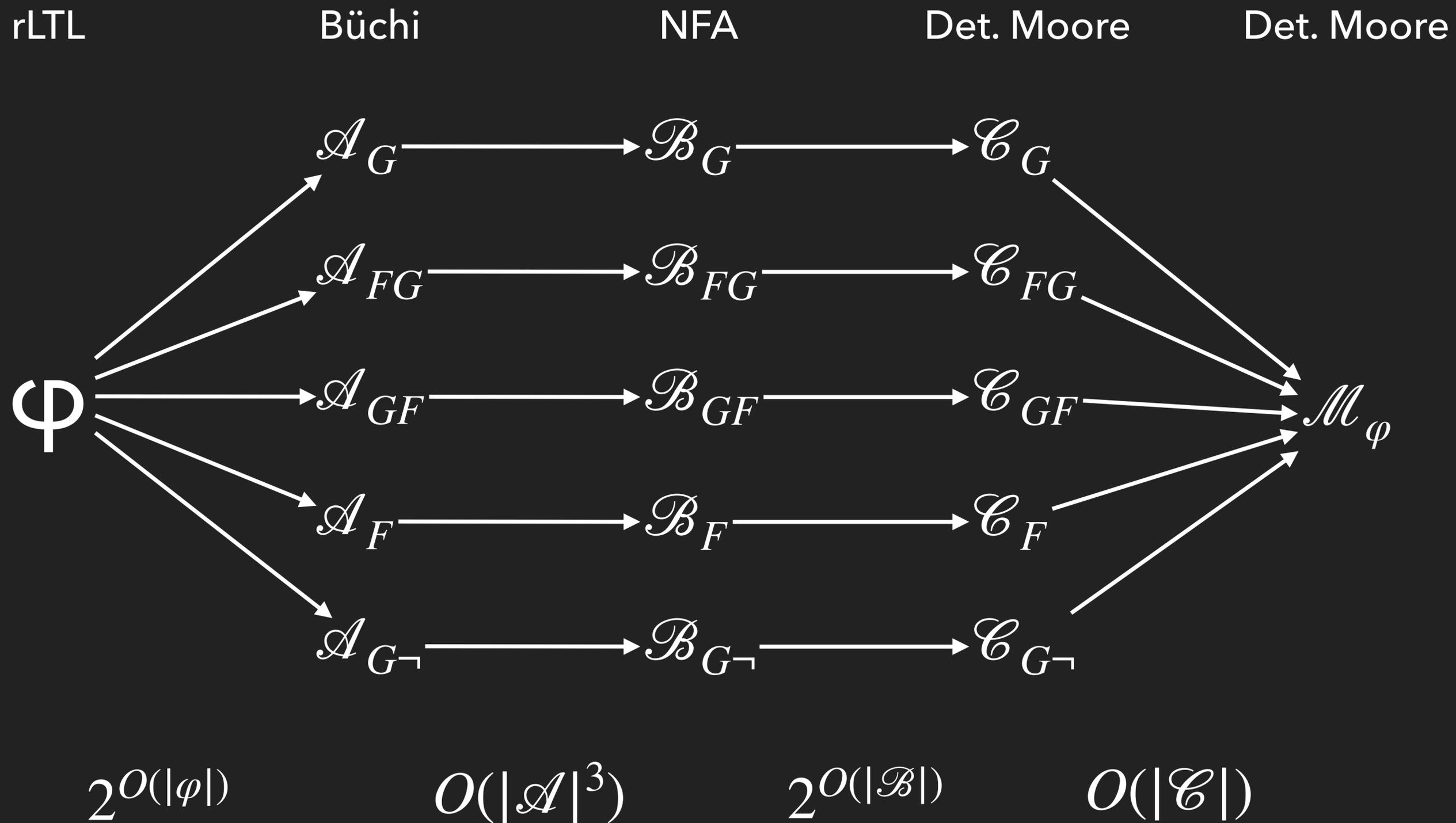
# Constructing an rLTL Monitor



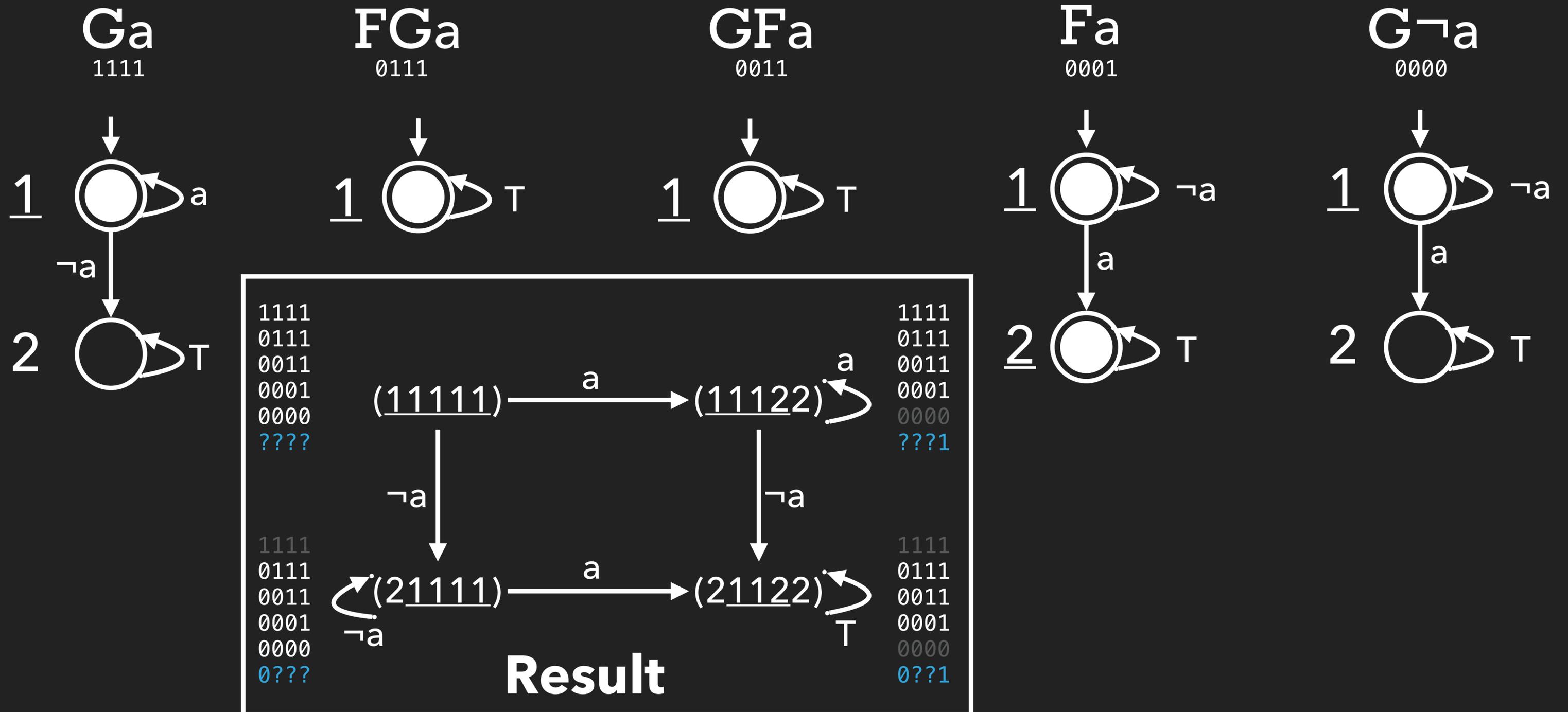
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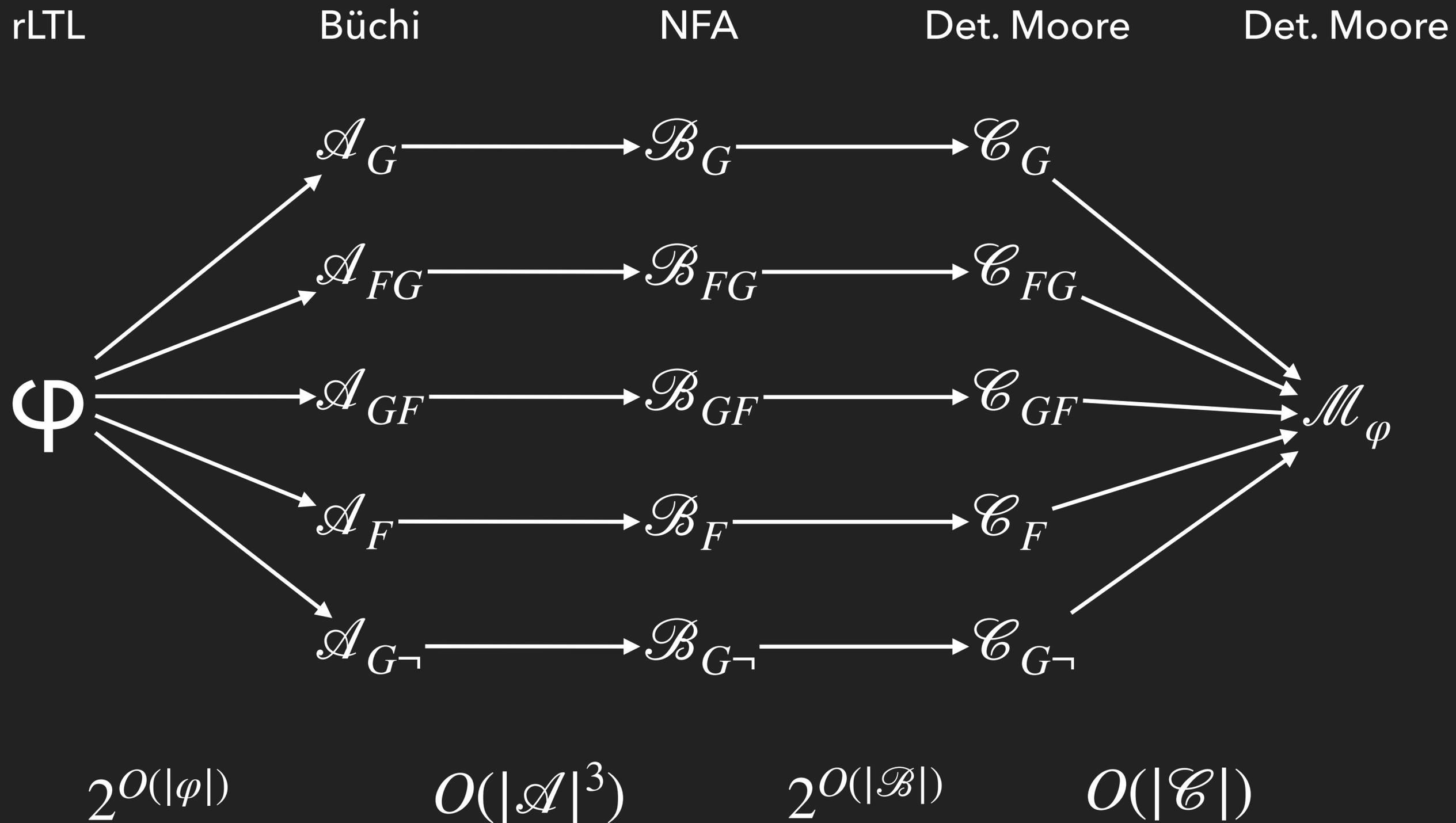
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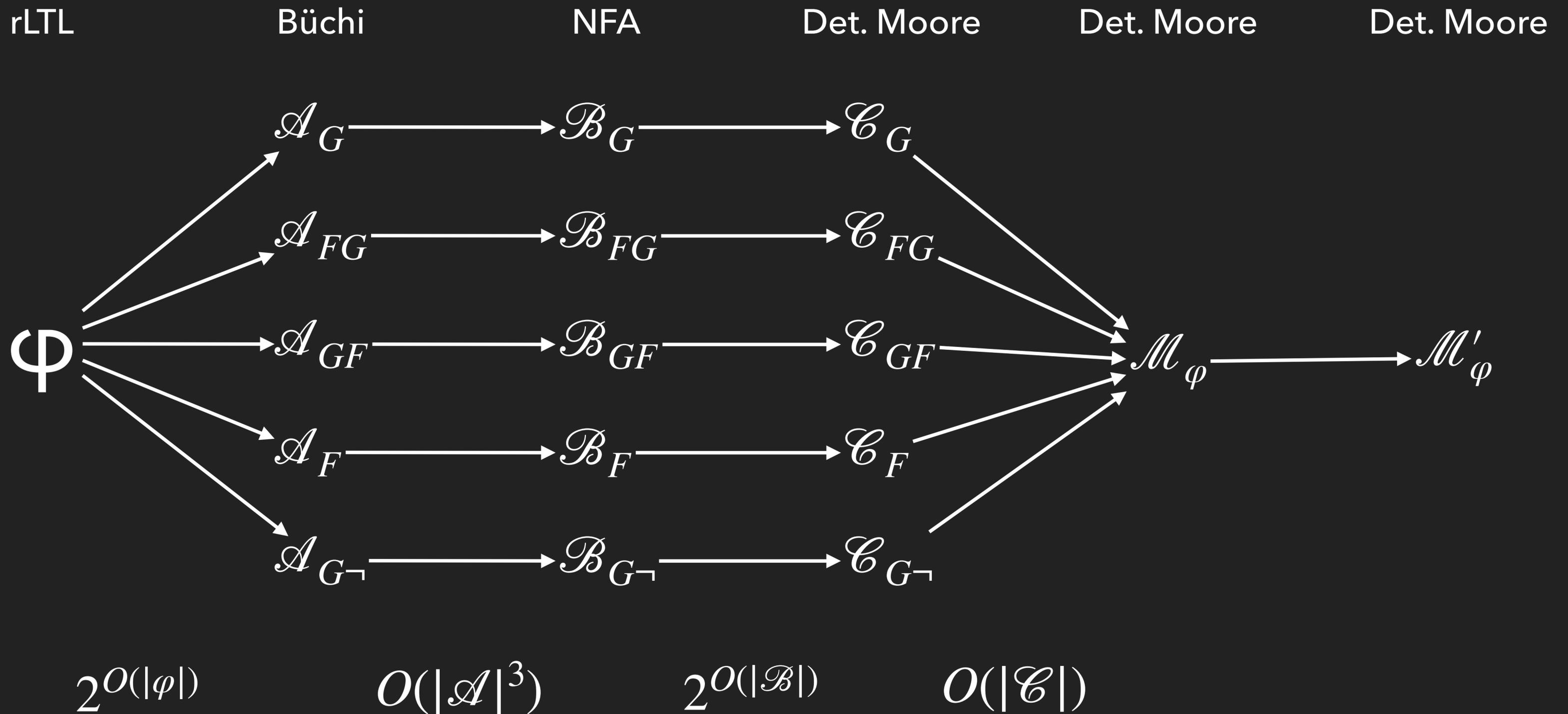
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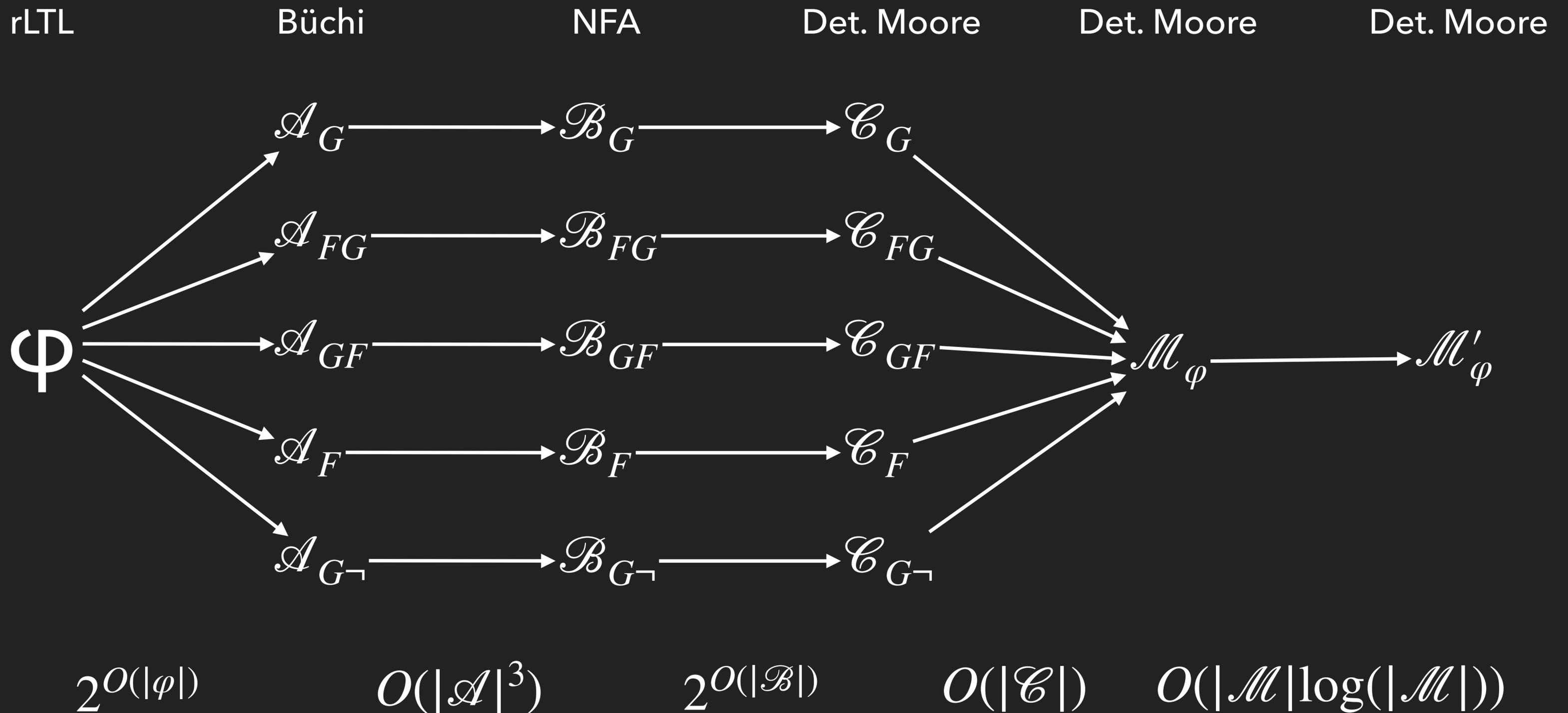
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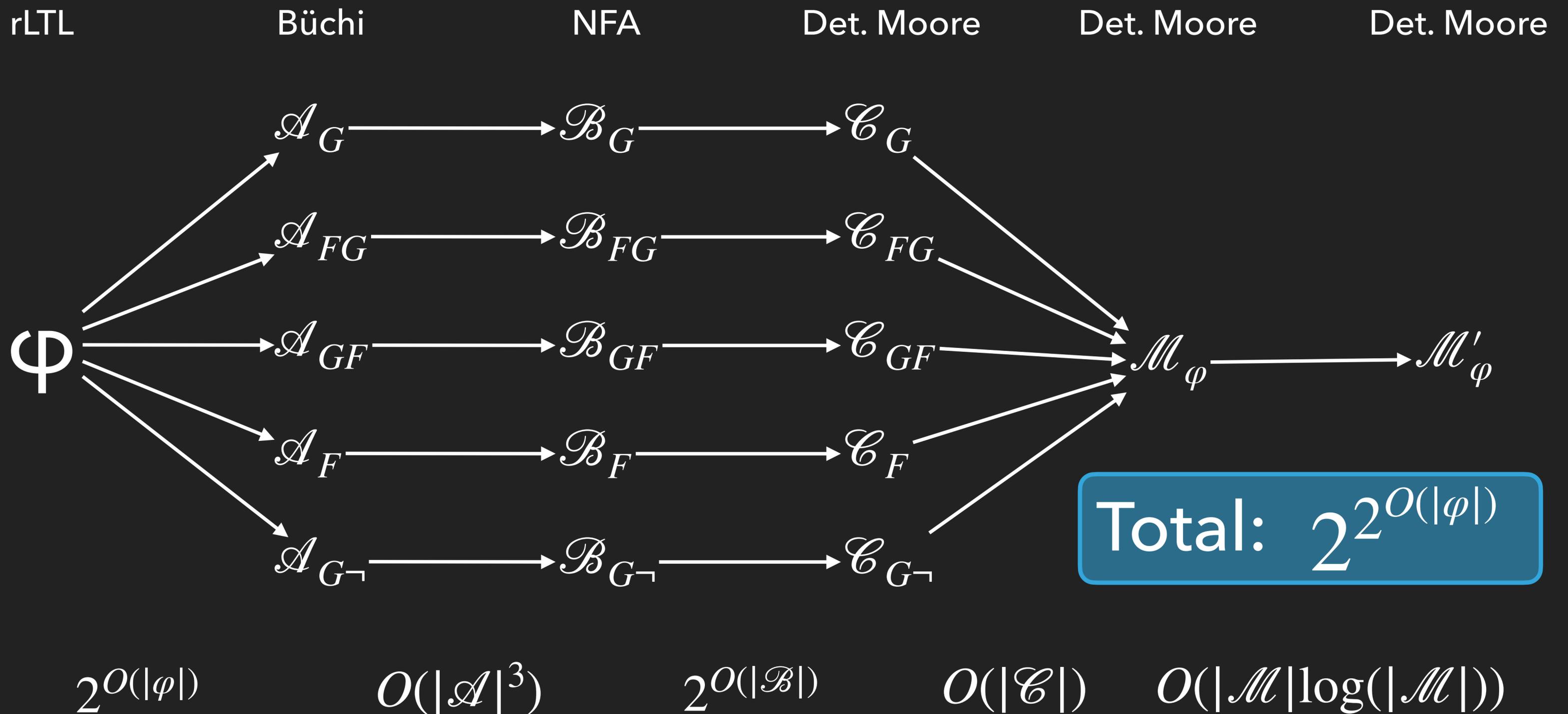
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# Benchmark

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Dwyer et al [1]:

97 LTL formulas

frequent specification patterns

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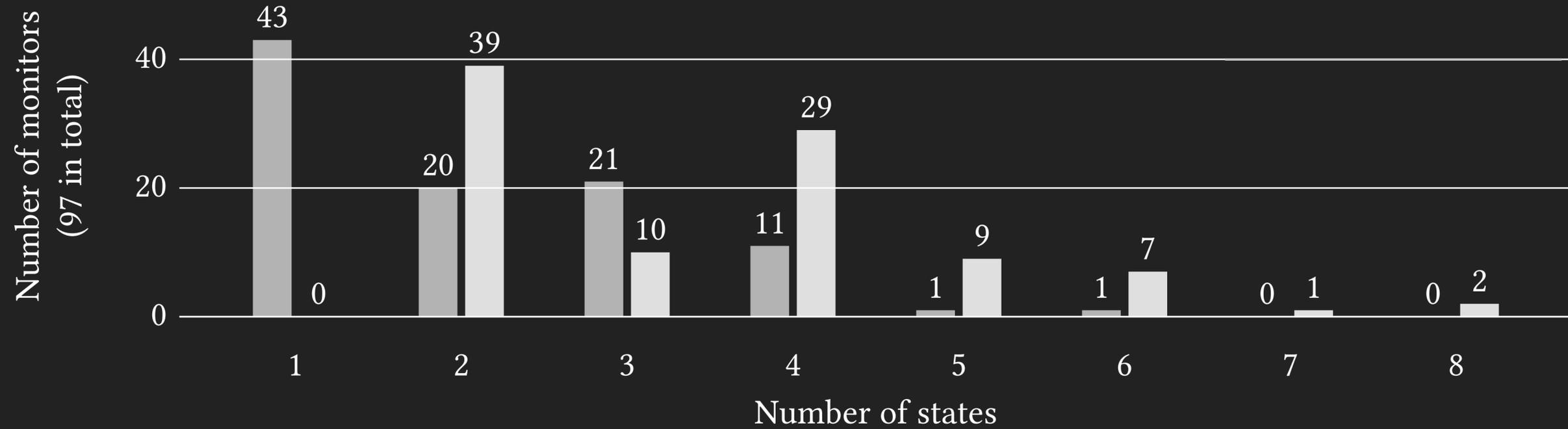
frequent specification patterns

**55.7% LTL**-monitorable [2] versus **100% rLTL**-monitorable

[1] Dwyer, Avrunin, Corbett. "Patterns in property specifications for finite-state verification". ICSE 1999

[2] Bauer, Leucker, Schallhart. "Runtime verification for LTL and TLTL". ACM Trans. Softw. Eng. Methodol. 2011

# Results

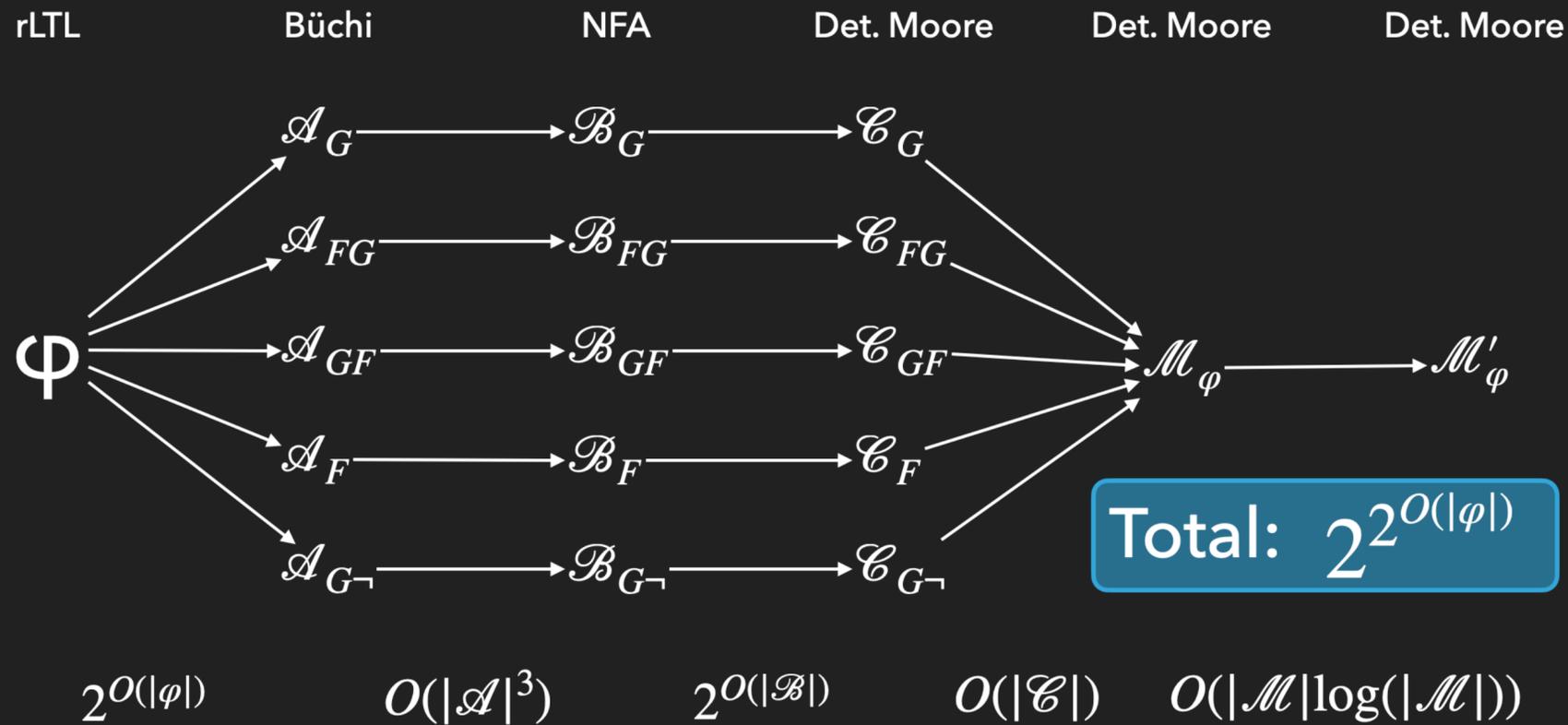


Histogram of the number of monitors with respect to their size

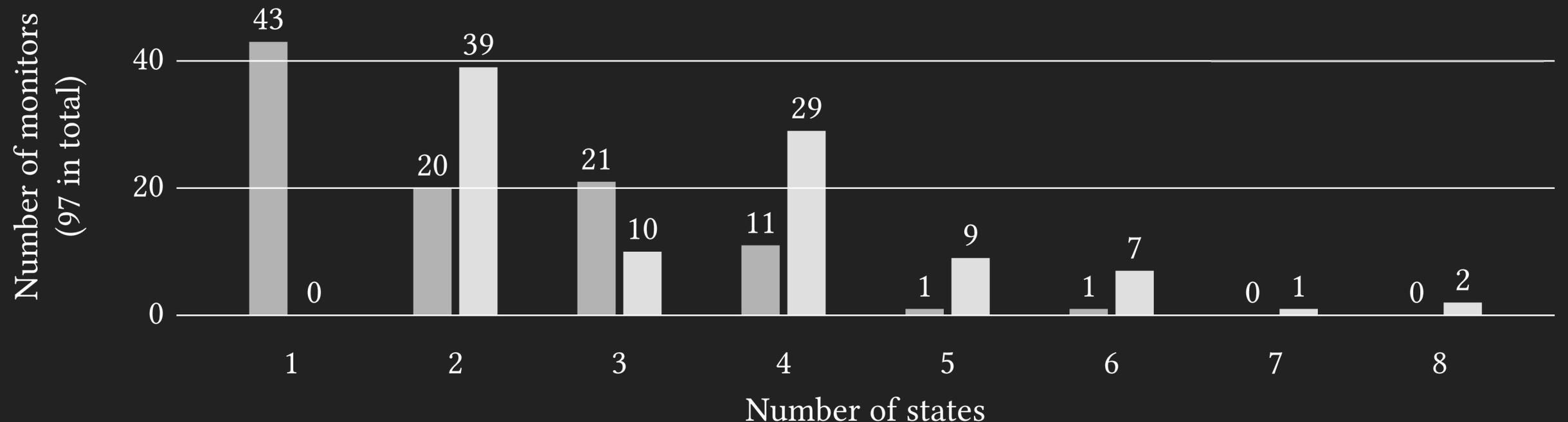


Analysis of the monitor construction for the 54 formulas that are both LTL-monitorable and rLTL-monitorable

# Summary

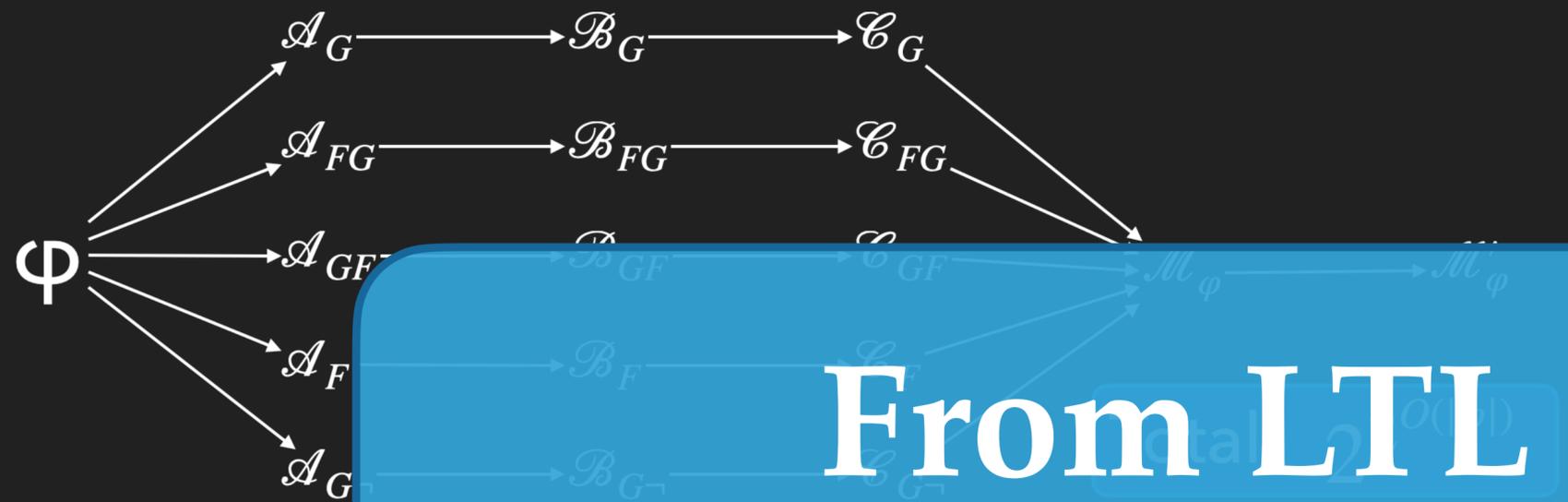


**55.7% LTL-monitorable**  
 versus  
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# Summary

rLTL      Büchi      NFA      Det. Moore      Det. Moore      Det. Moore



**55.7% LTL-monitorable**

versus

**100% rLTL-monitorable**

**From LTL to rLTL:  
More Information;  
Same (Asymptotic) Cost**

